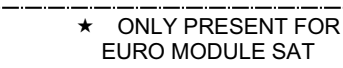
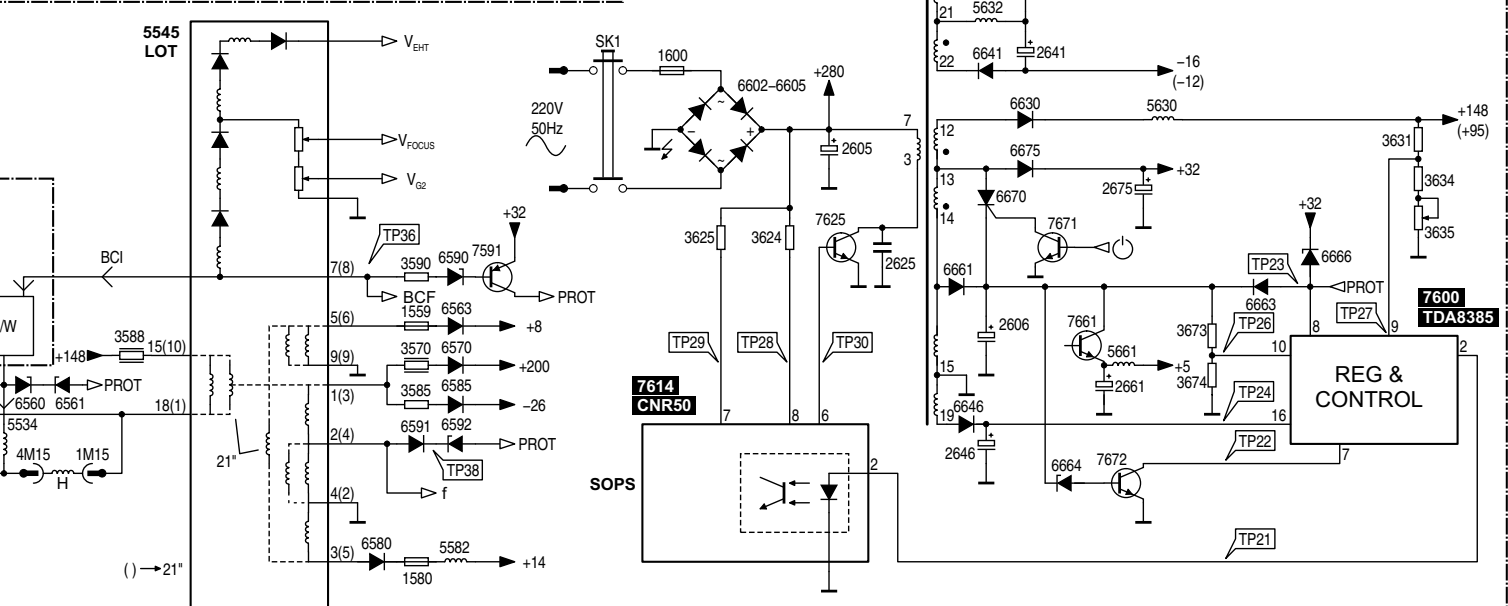
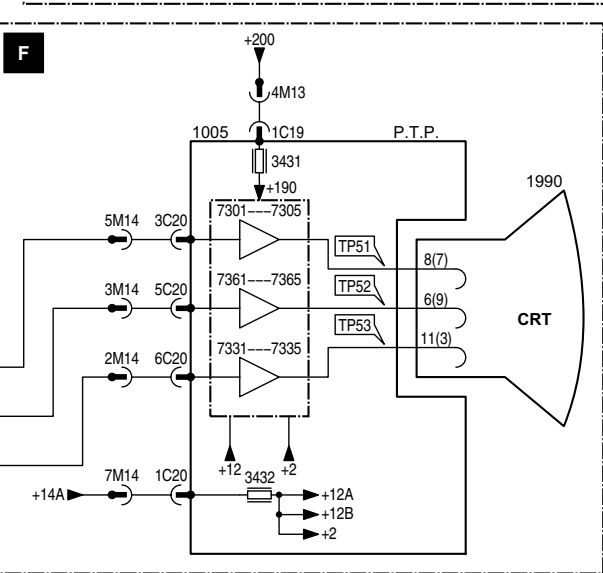
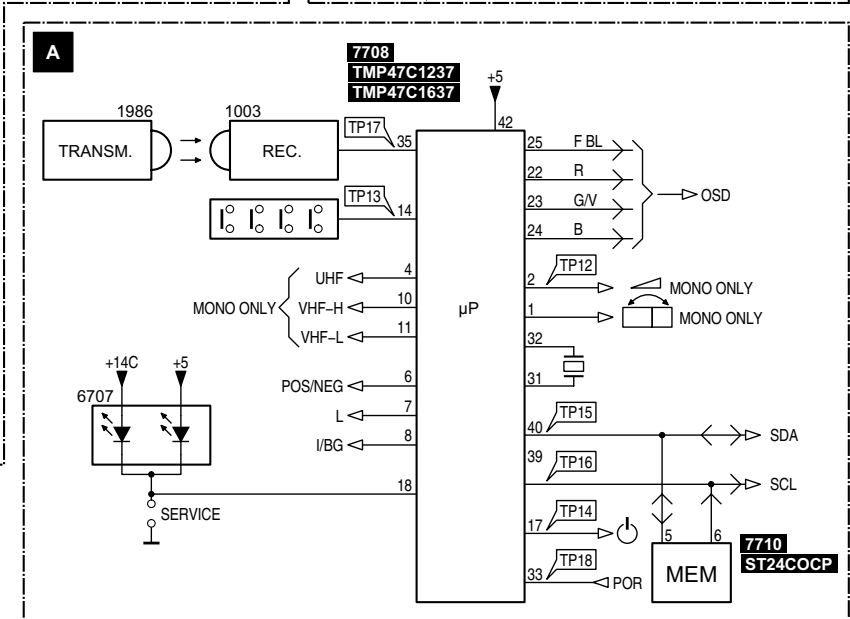
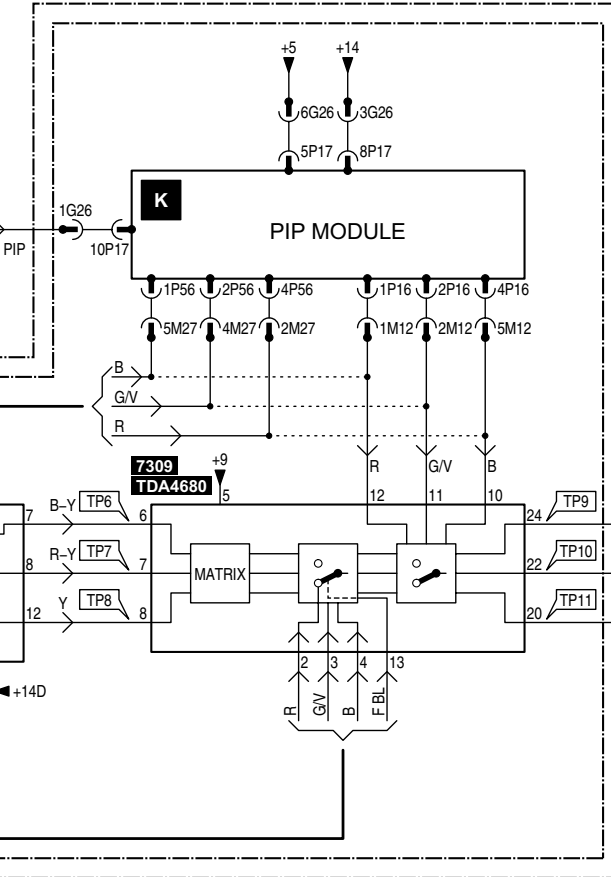
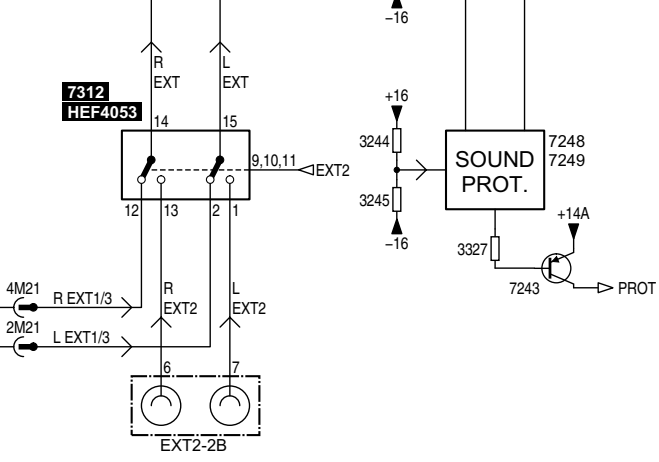
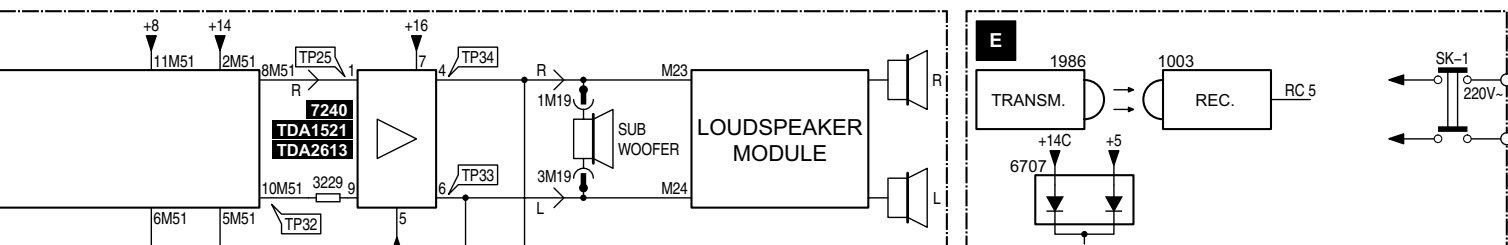
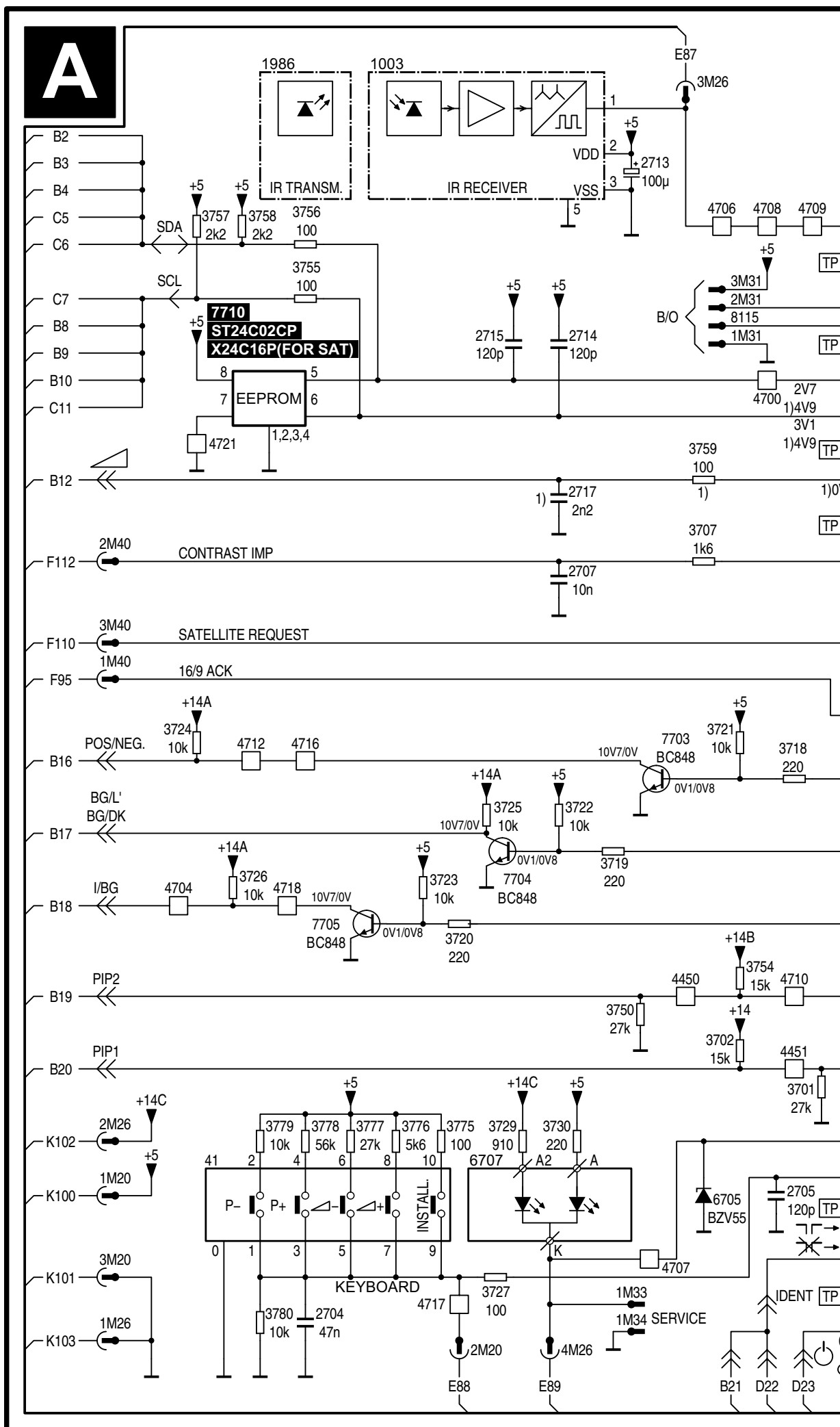


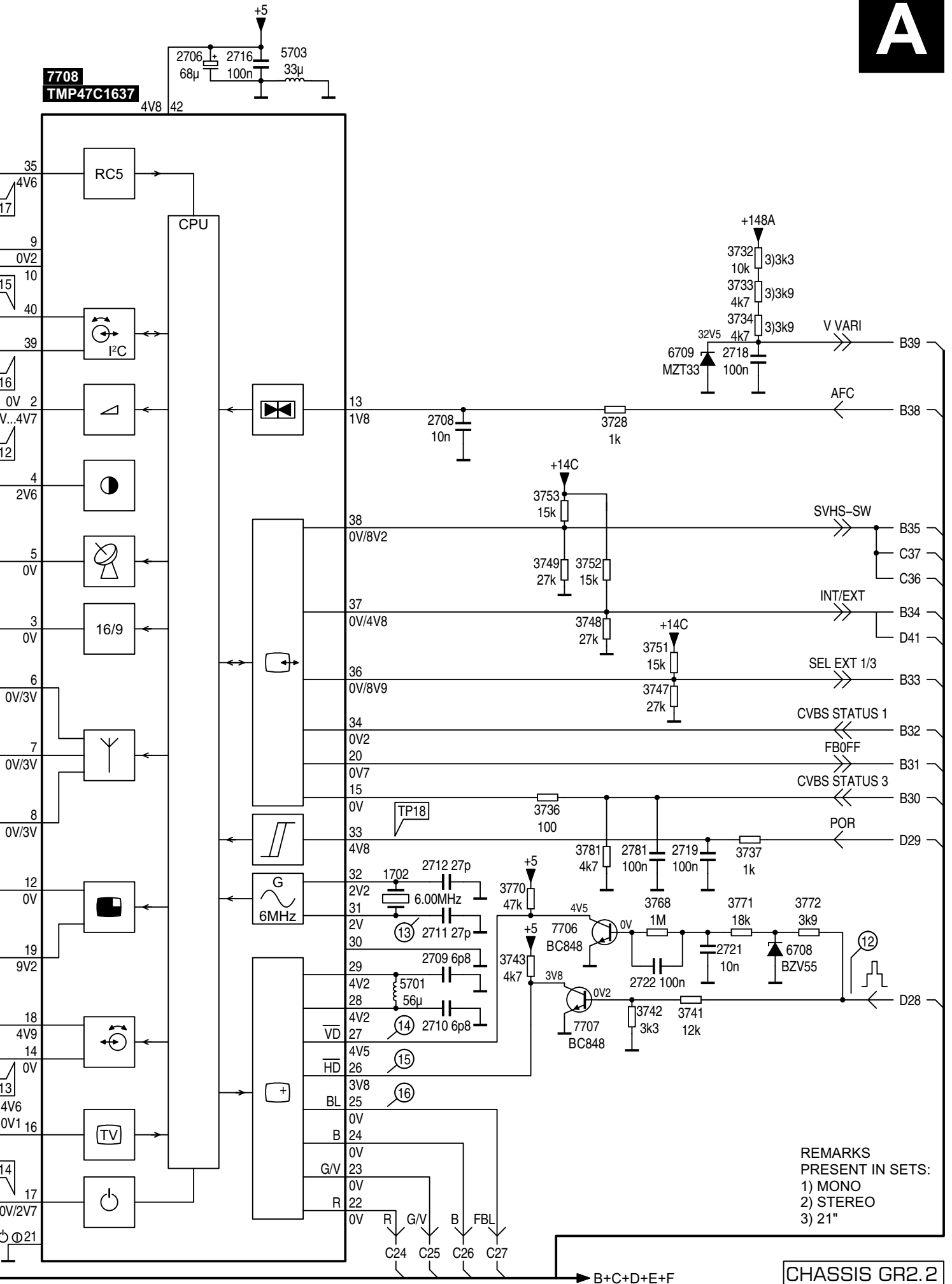
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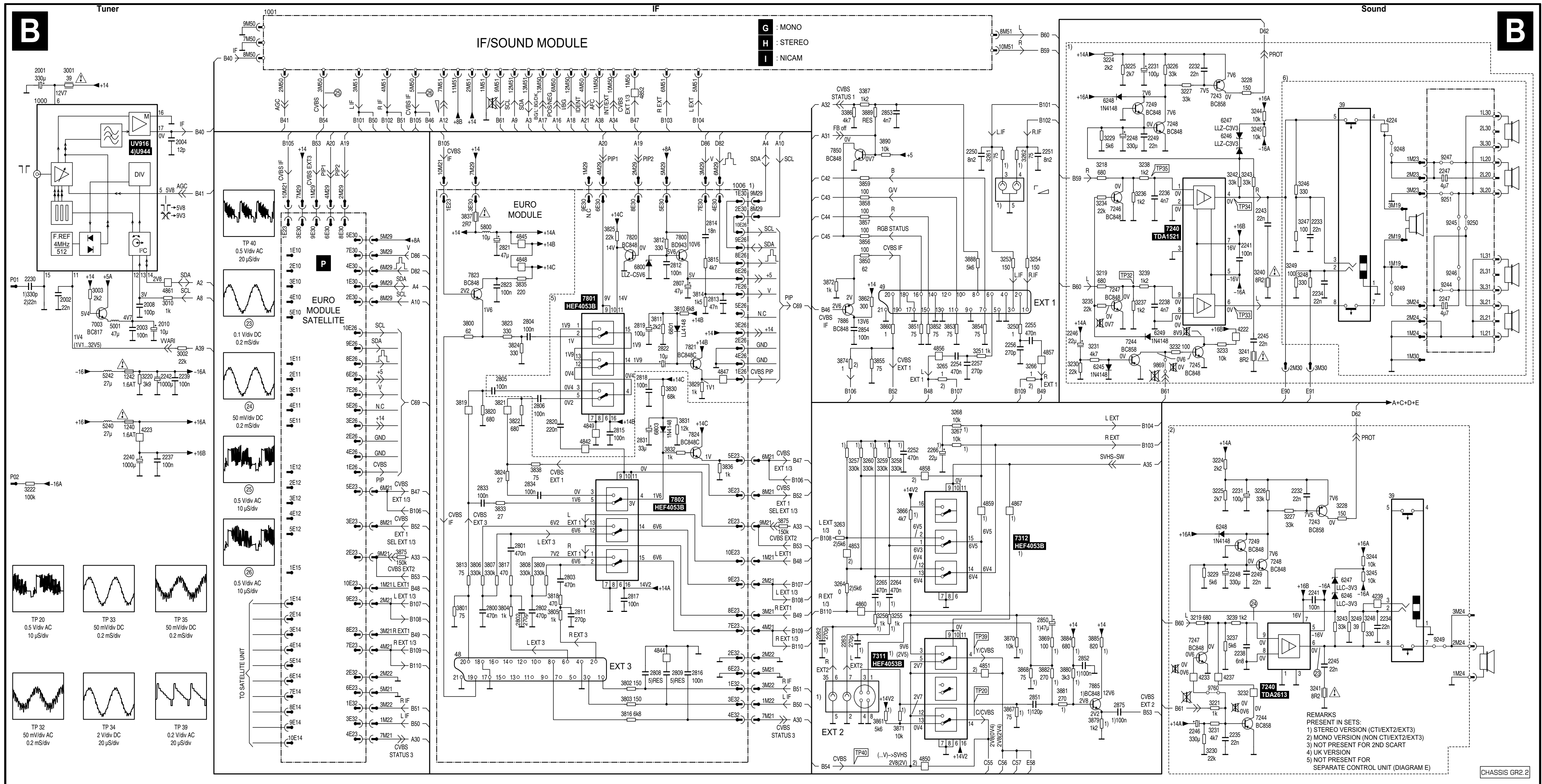




OTVC Philips chassis GR2.2 *strona 1/6*







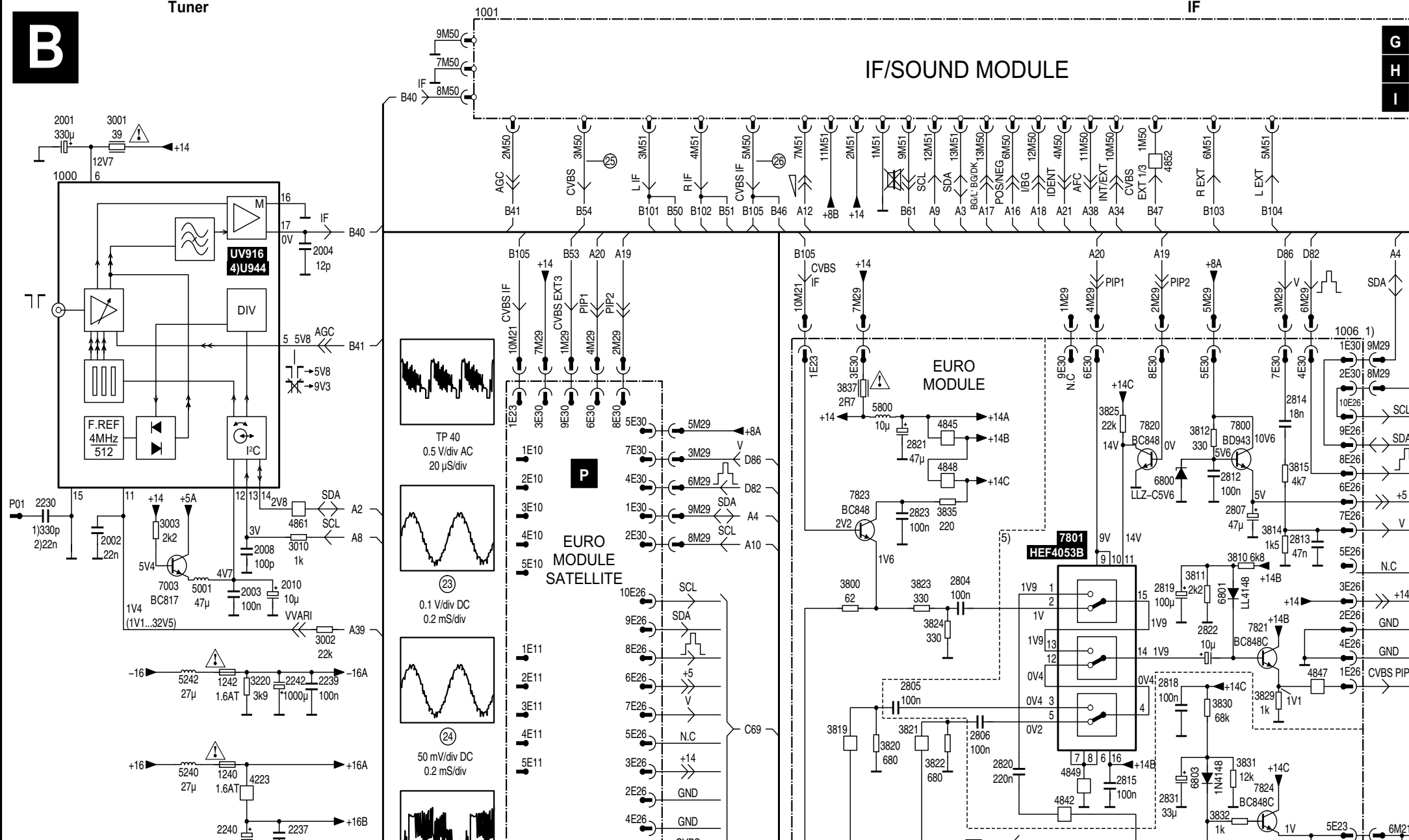
B

Tuner

IF

G
H
I

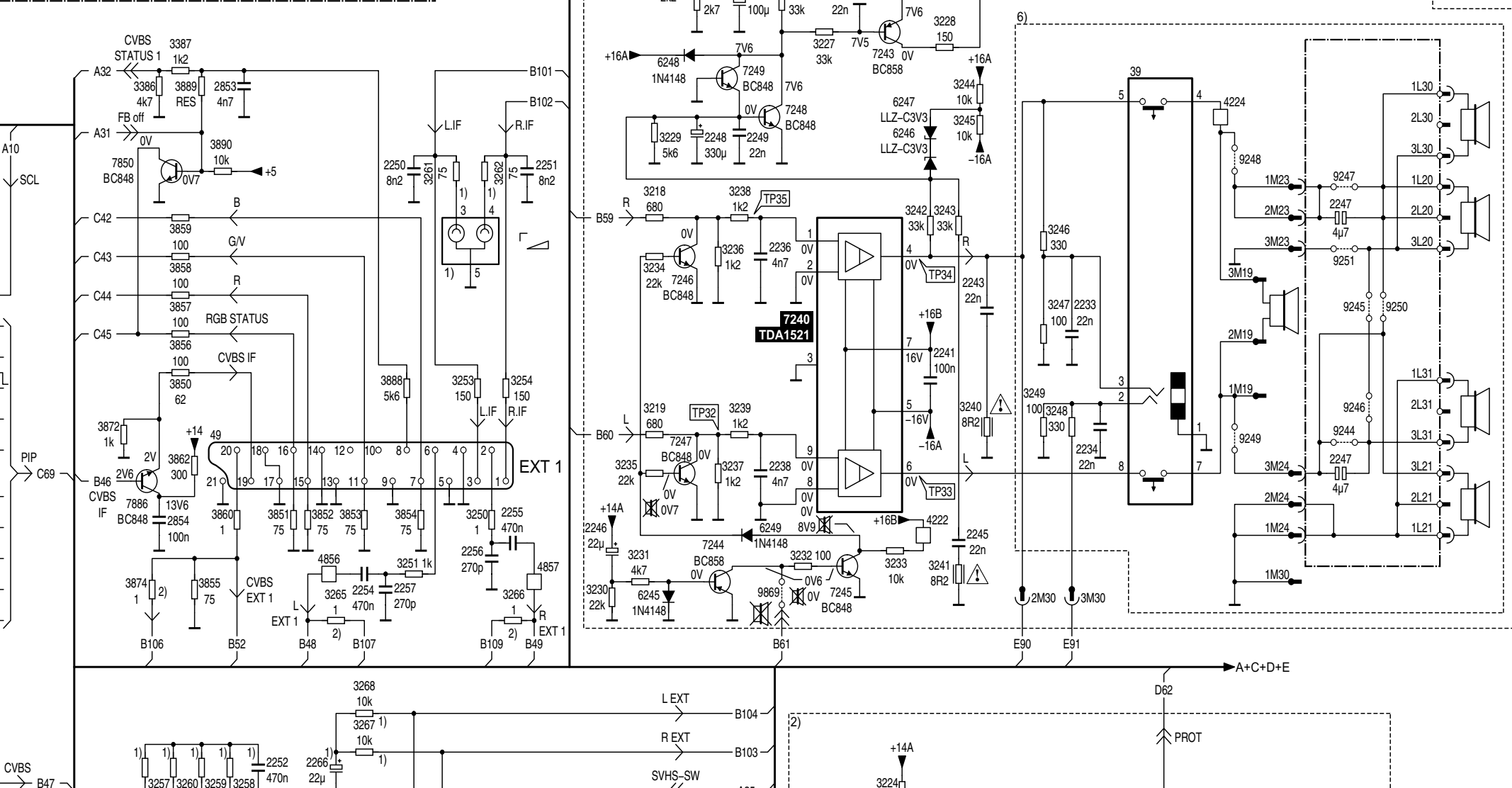
IF/SOUND MODULE

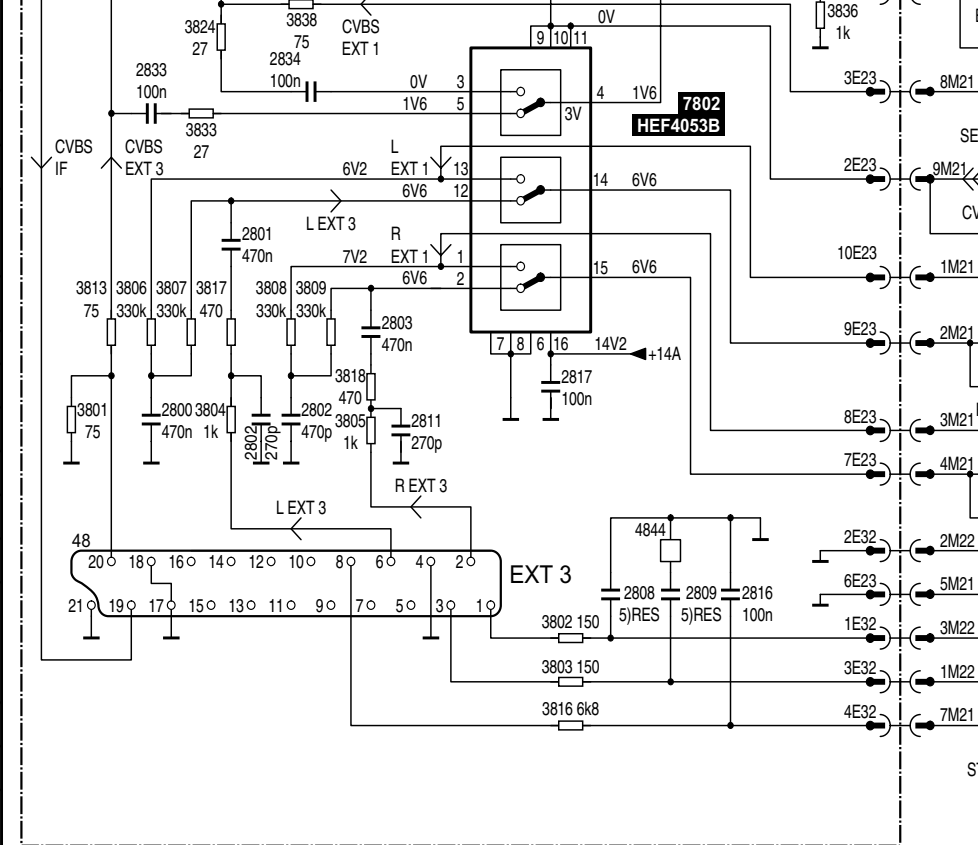


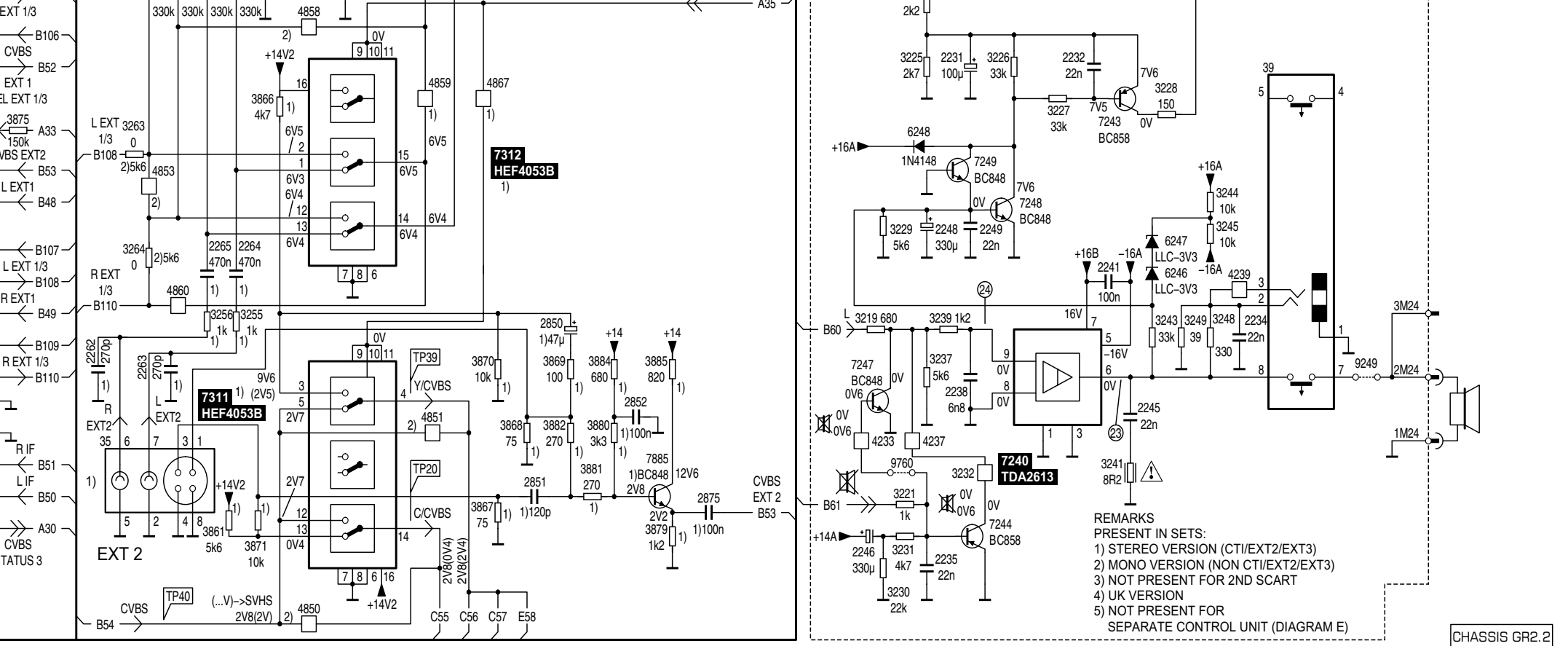
: MONO
: STEREO
: NICAM

Sound

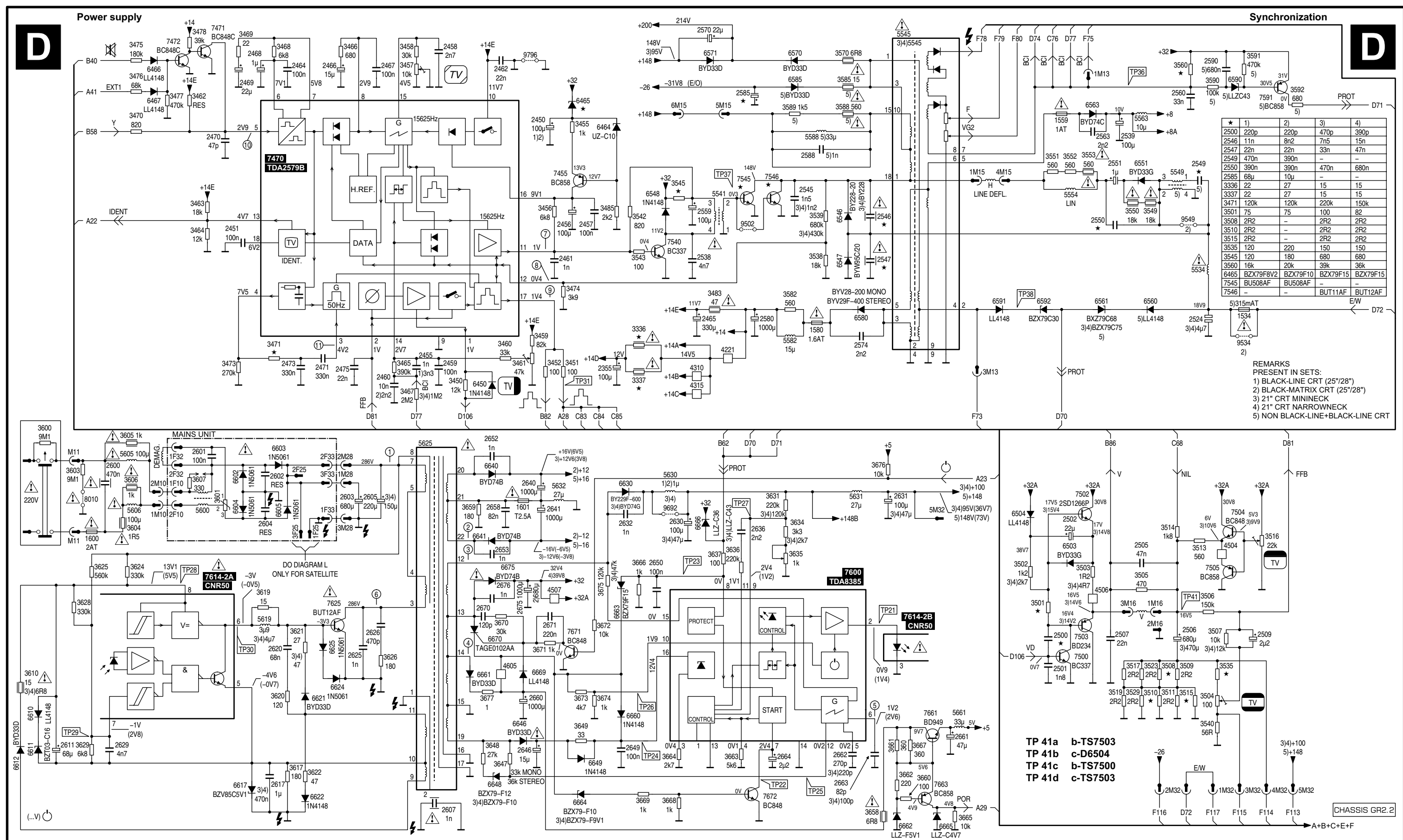
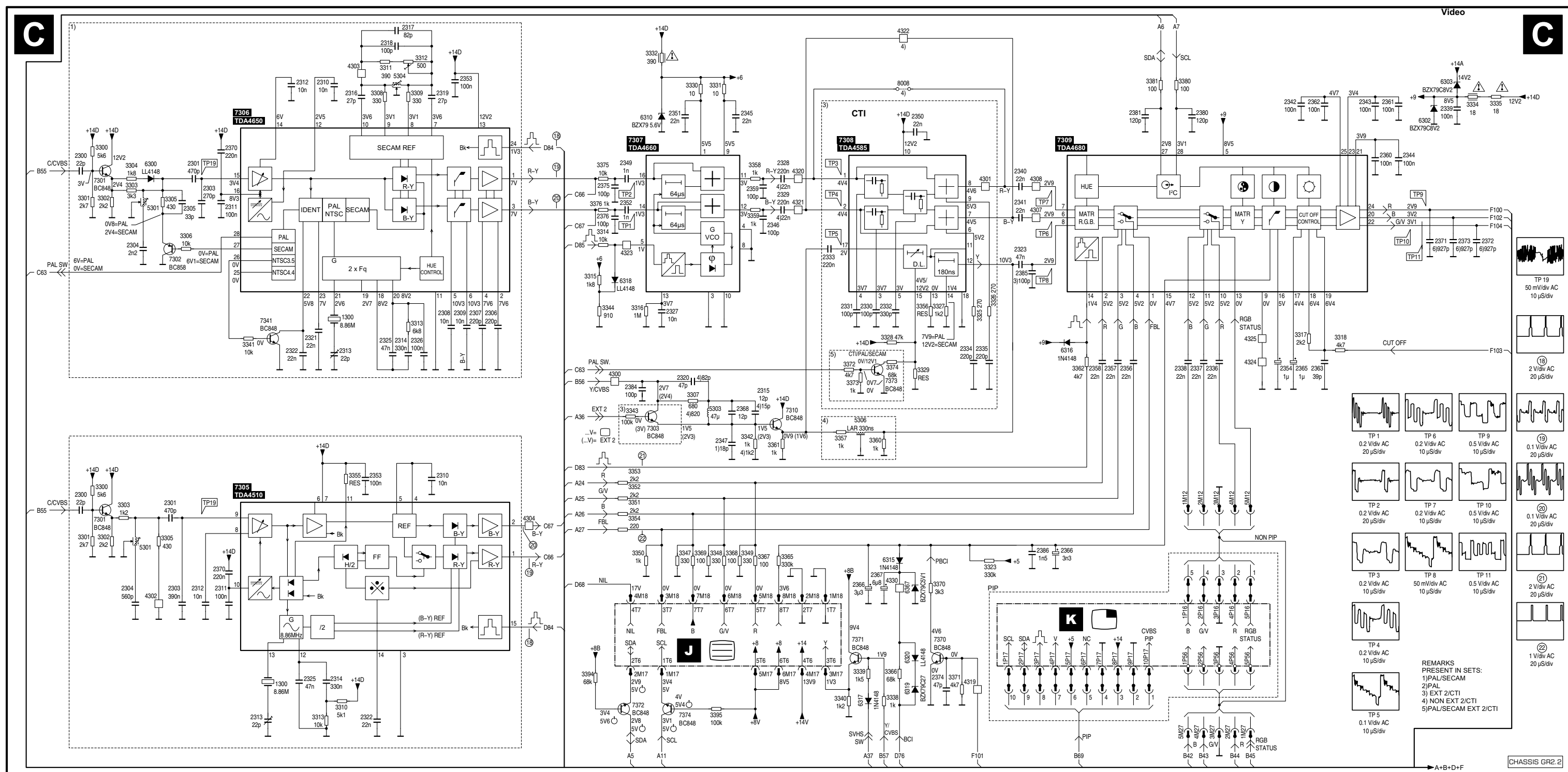
B







OTVC Philips chassis GR2.2 strona 2/6

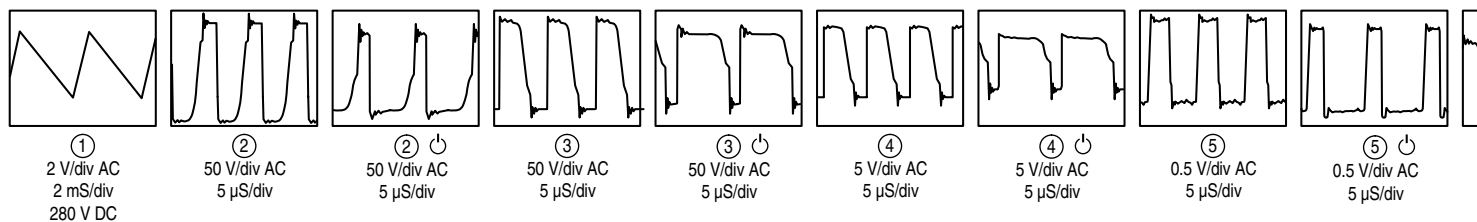
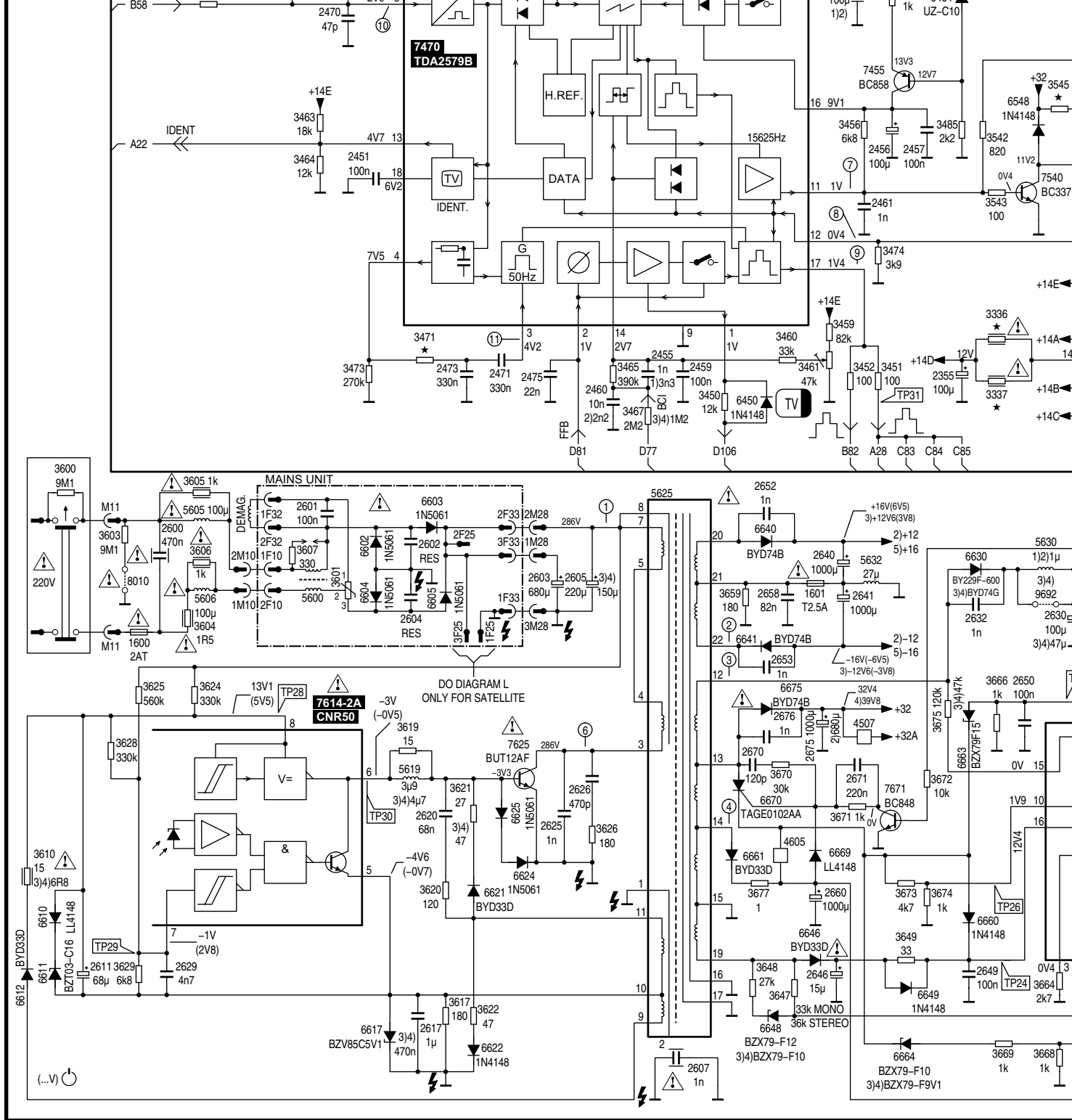


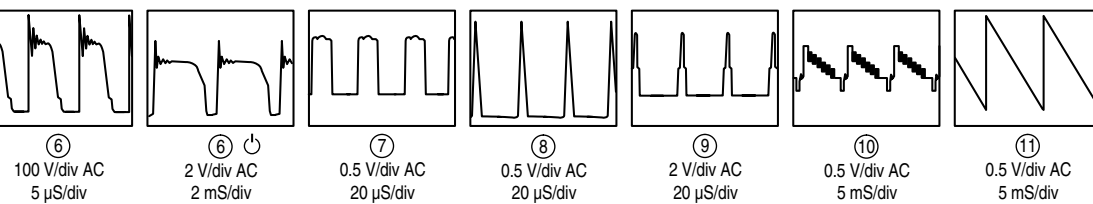
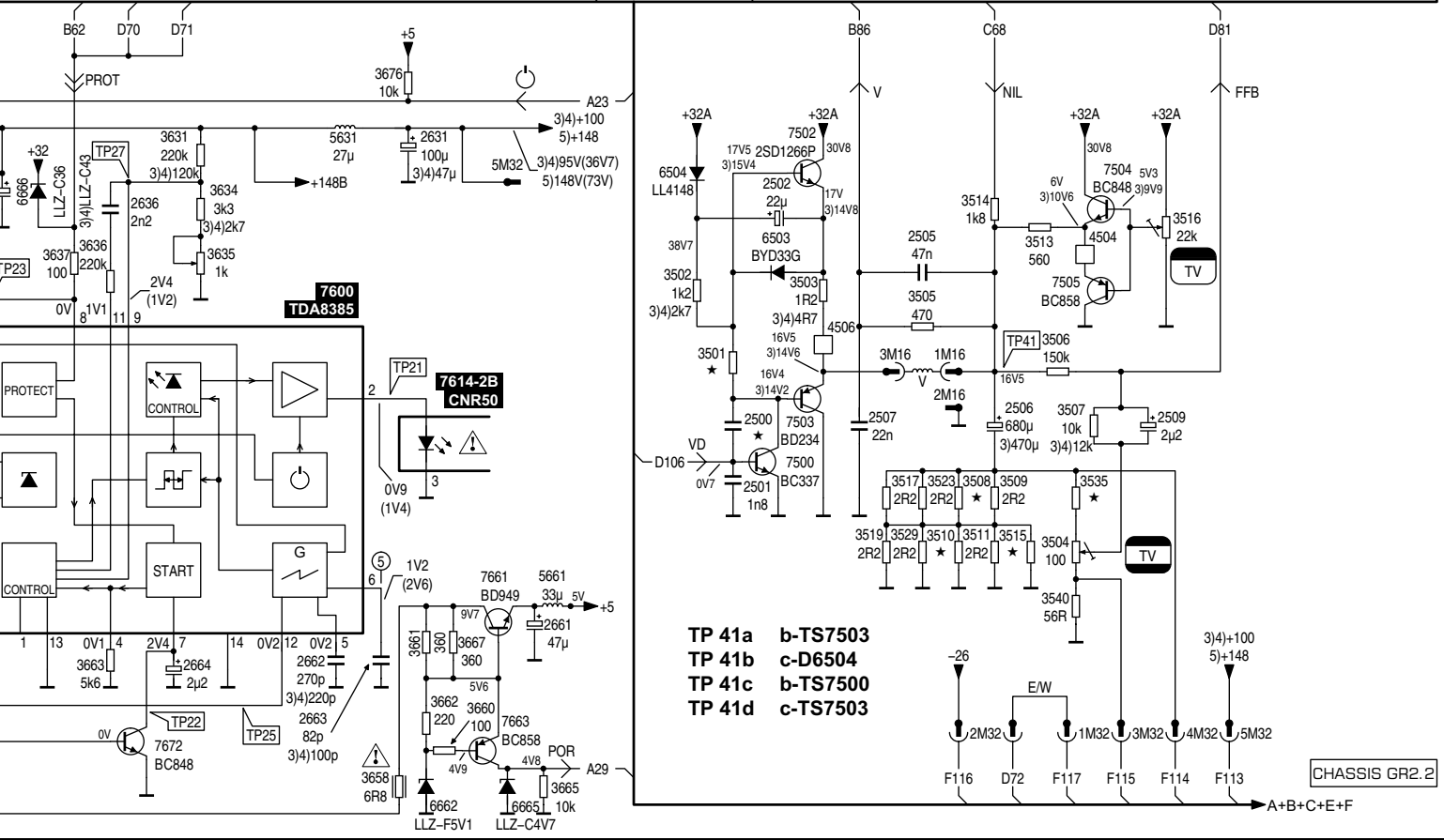
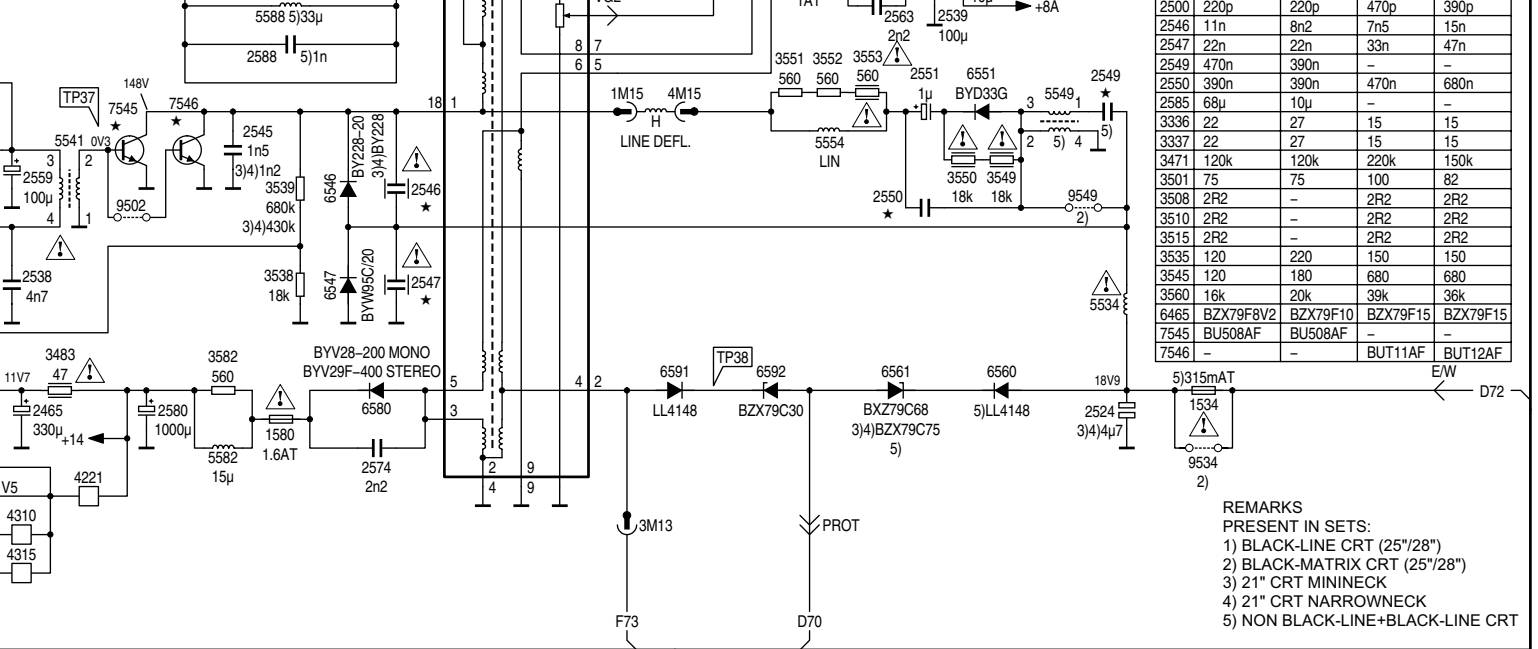
The diagram illustrates a video decoder circuit, likely for a portable video player or a specialized receiver. It is divided into several functional blocks:

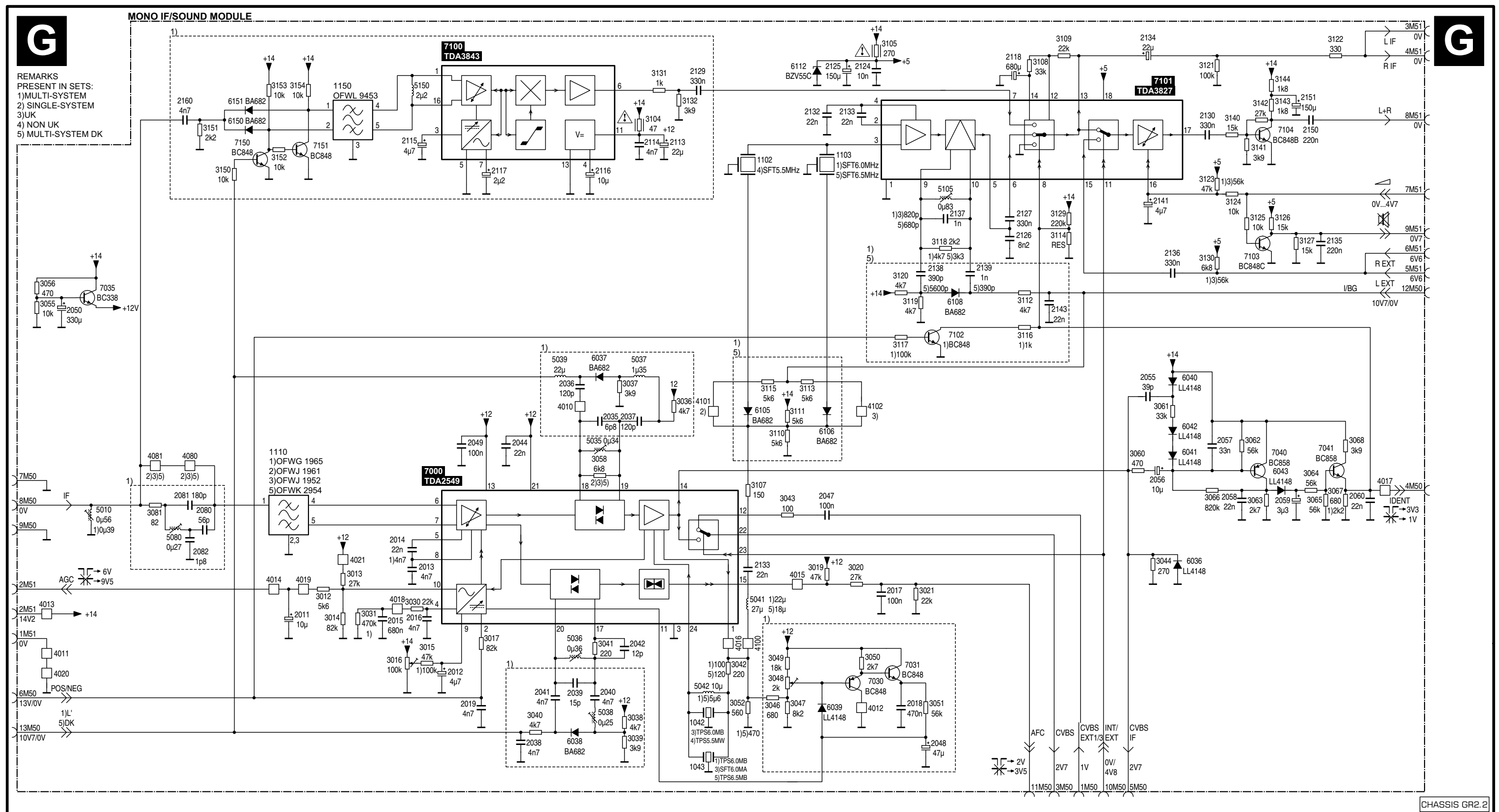
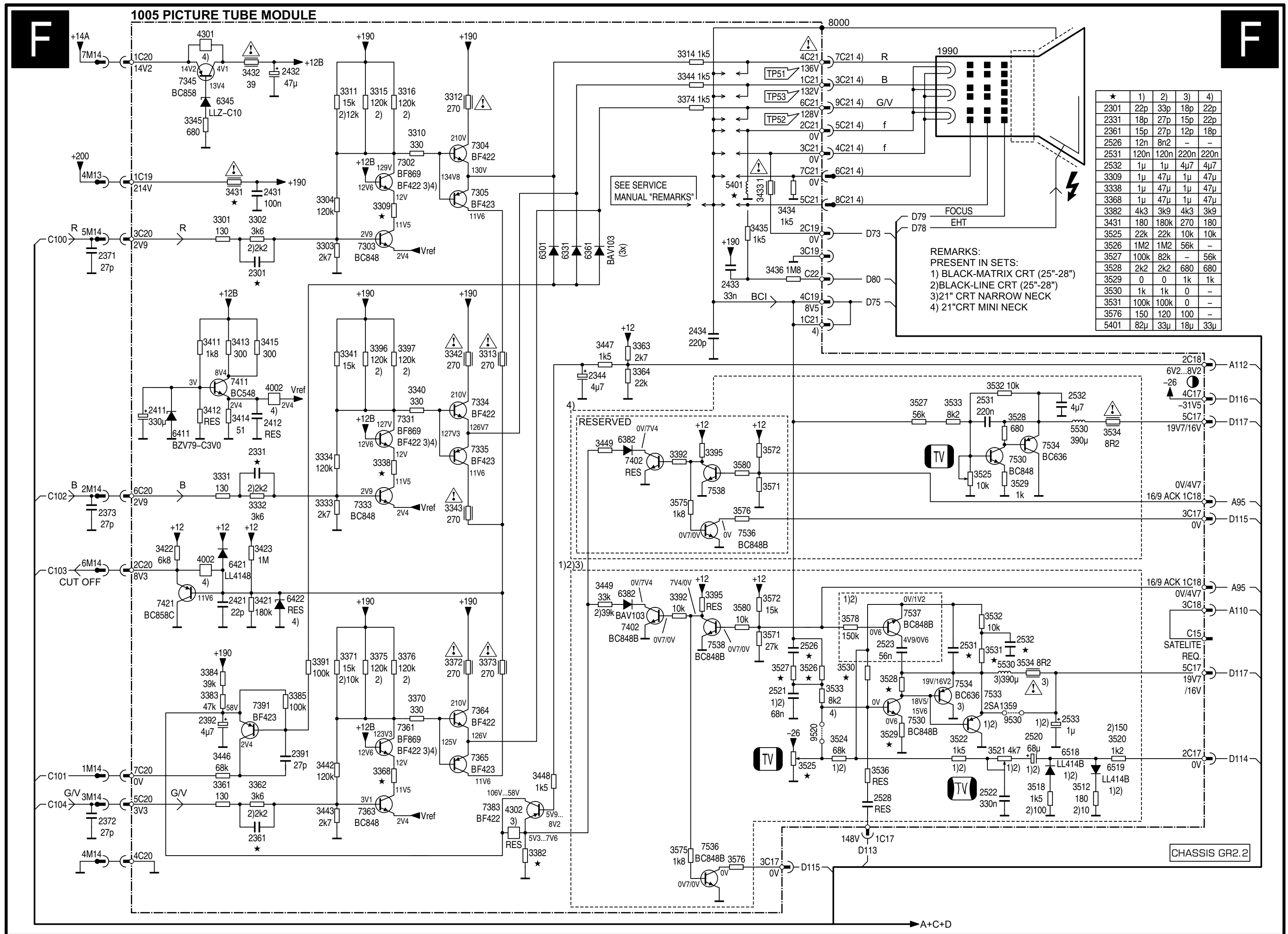
- TDA4650 (Top Left):** This IC handles the initial signal processing. It includes a **SECAM REF** block, a **2 x Fq** (frequency doubler) block, and a **HUE CONTROL** block. It also features a **SECAM** block and a **NTSC3.5** block. The circuit includes various input signals like **C/CVBS** and **PAL SW**, and is populated with numerous resistors and capacitors for timing and signal conditioning.
- TDA4660 (Top Right):** This IC is responsible for color processing. It includes a **G VCO** (Color Subcarrier Oscillator) block, a **64μs** (64 microsecond) block, and a **64μs** (64 microsecond) block. It also features a **SECAM** block and a **NTSC3.5** block. The circuit includes various input signals like **C/CVBS** and **PAL SW**, and is populated with numerous resistors and capacitors for timing and signal conditioning.
- TDA4510 (Bottom Left):** This IC is responsible for the final video output. It includes a **REF** (Reference) block, a **FF** (Frame Advance) block, and a **H/2** (Half-Line) block. It also features a **SECAM** block and a **NTSC3.5** block. The circuit includes various input signals like **C/CVBS** and **PAL SW**, and is populated with numerous resistors and capacitors for timing and signal conditioning.
- Video Display Output (Bottom Right):** This section shows the connection to a video display. It includes a **J** logo, a **SDA** (Serial Data) signal, a **SCL** (Serial Clock) signal, and a **FBL** (Frame Buffer) signal. It also includes a **5V4** (5.4V) power supply and a **3V4** (3.4V) power supply.

The circuit is highly detailed, showing the internal block structures of the ICs and the external components required for proper operation. It includes a variety of components such as resistors, capacitors, transistors, and integrated circuits, all interconnected to form a complete video decoder system.

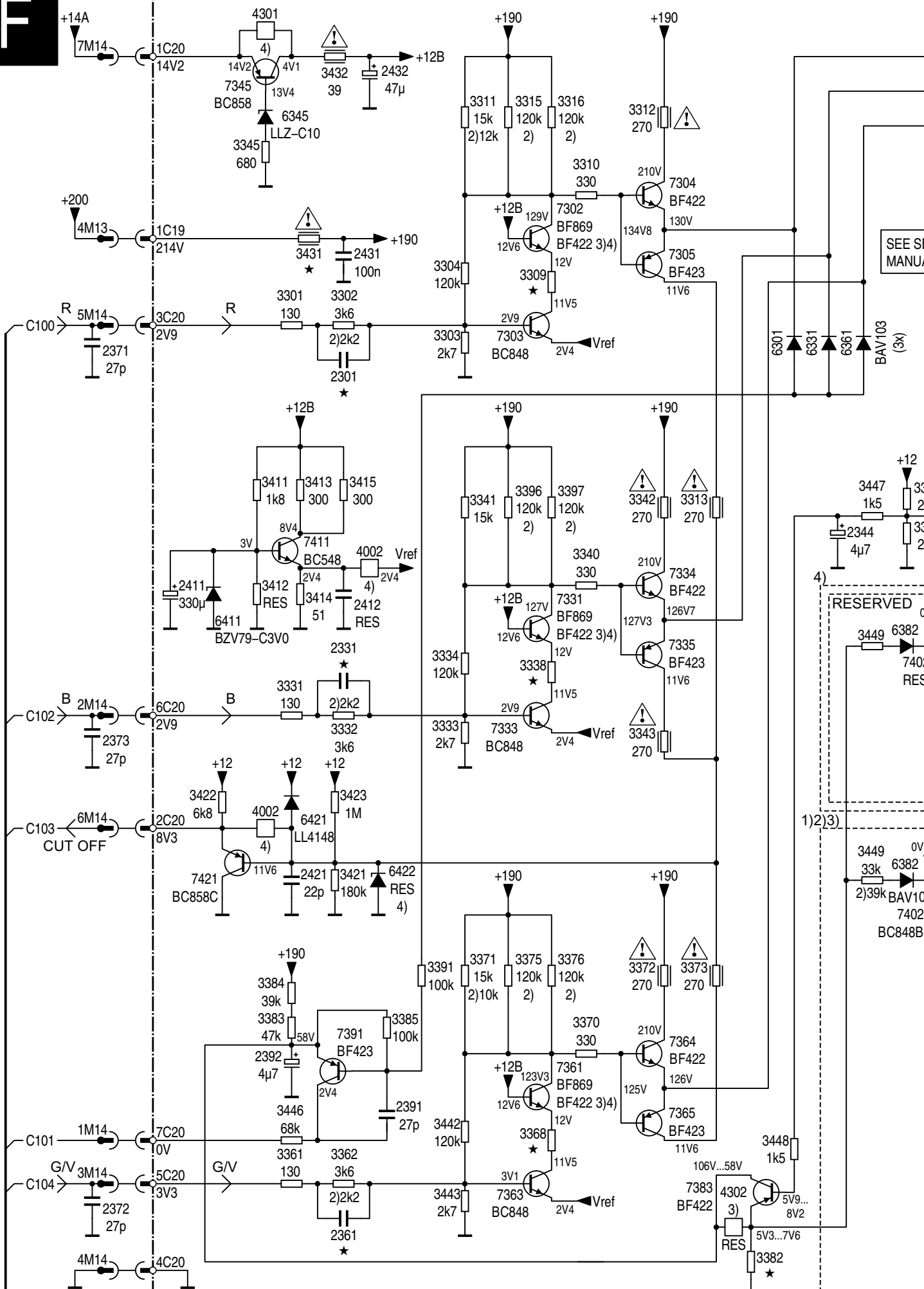
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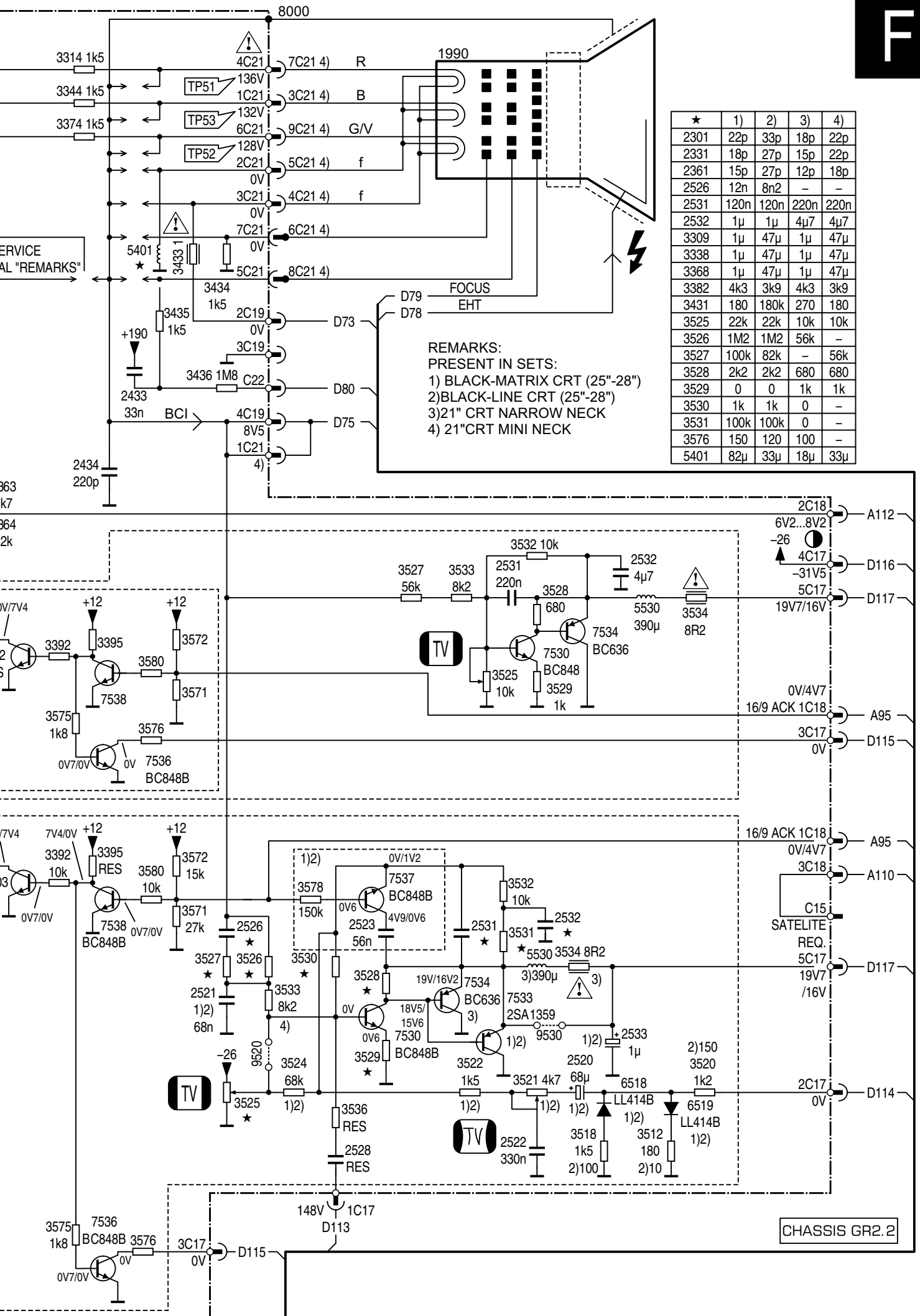




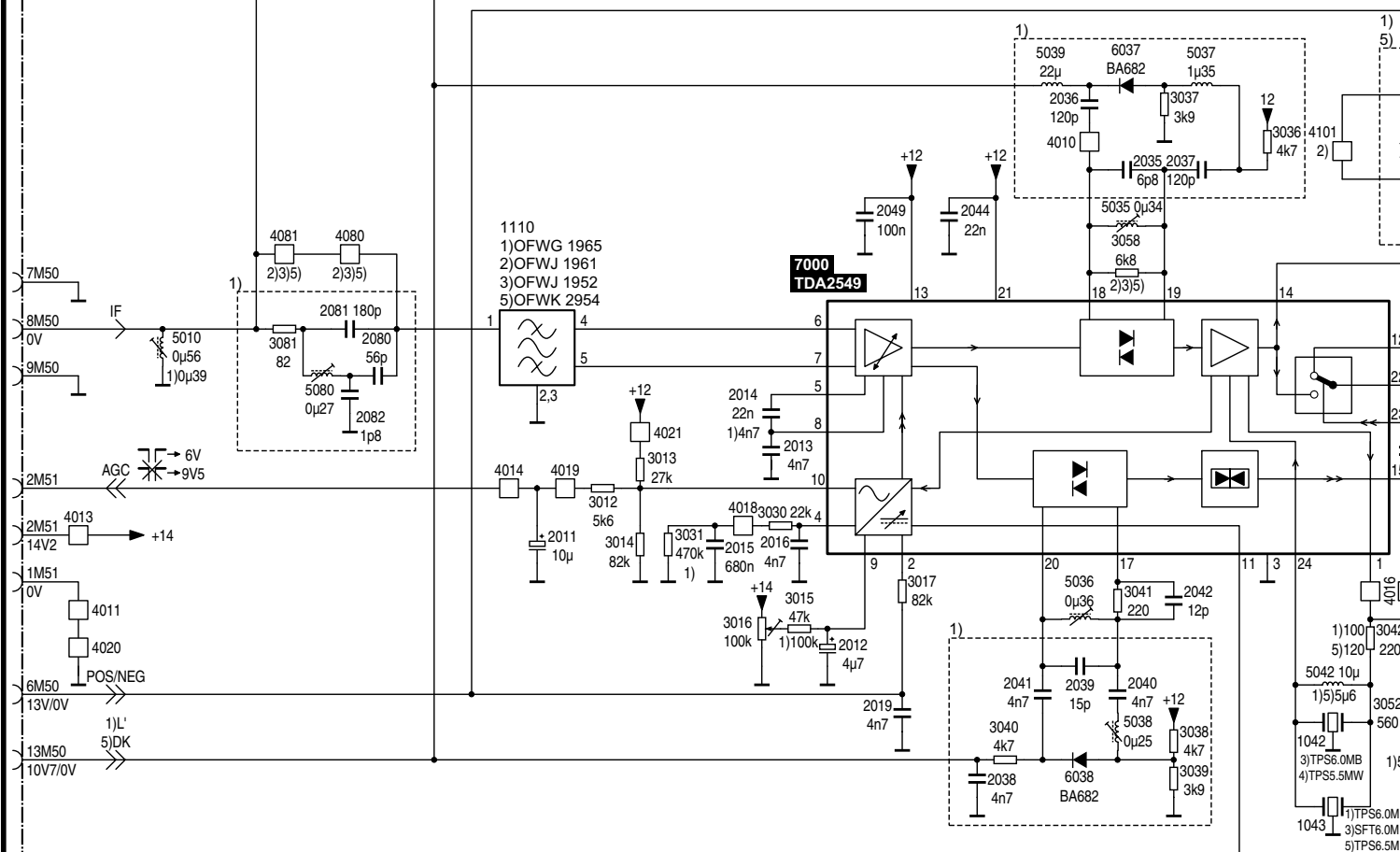
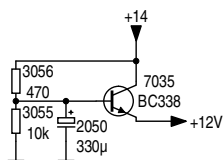


F

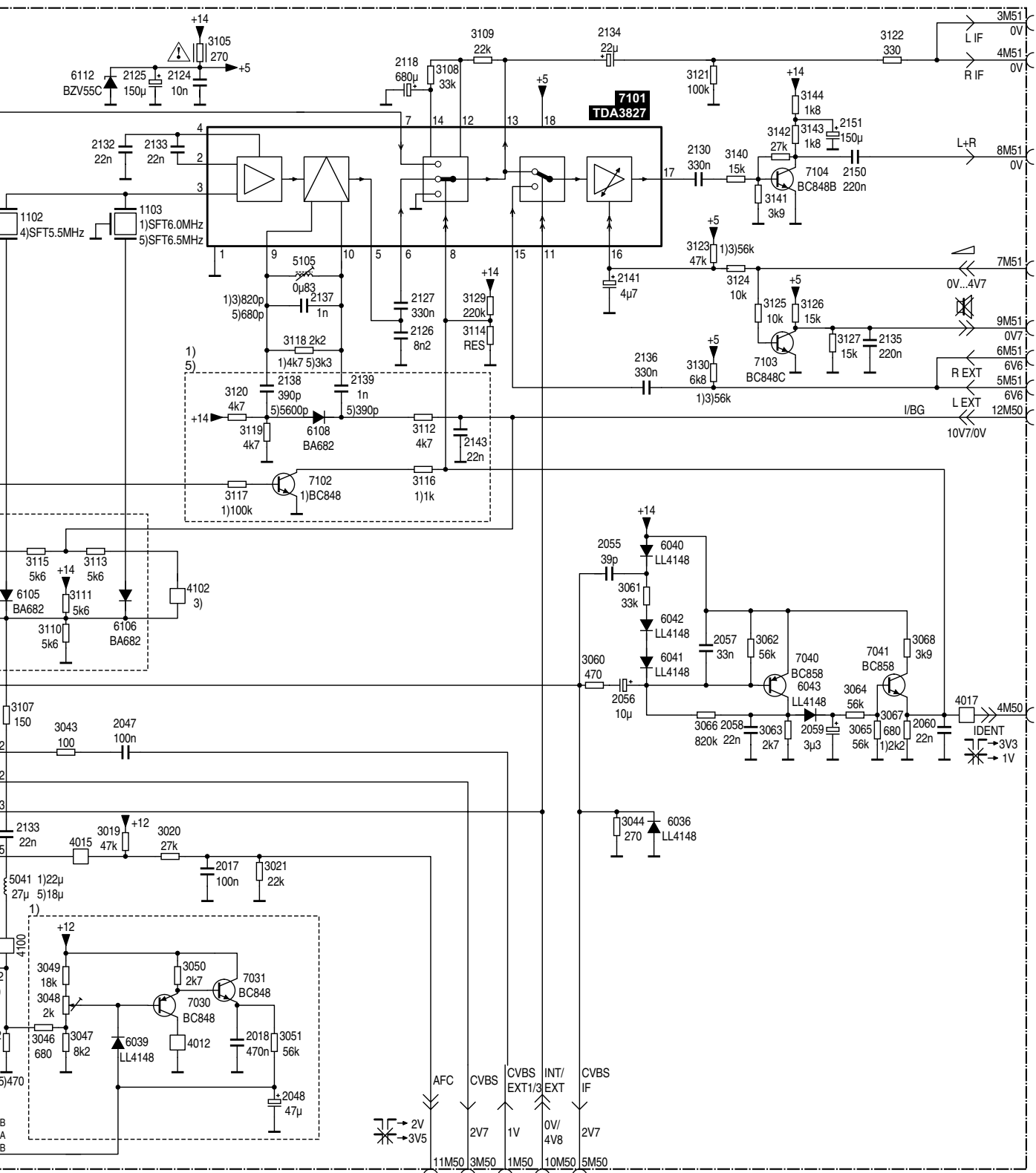


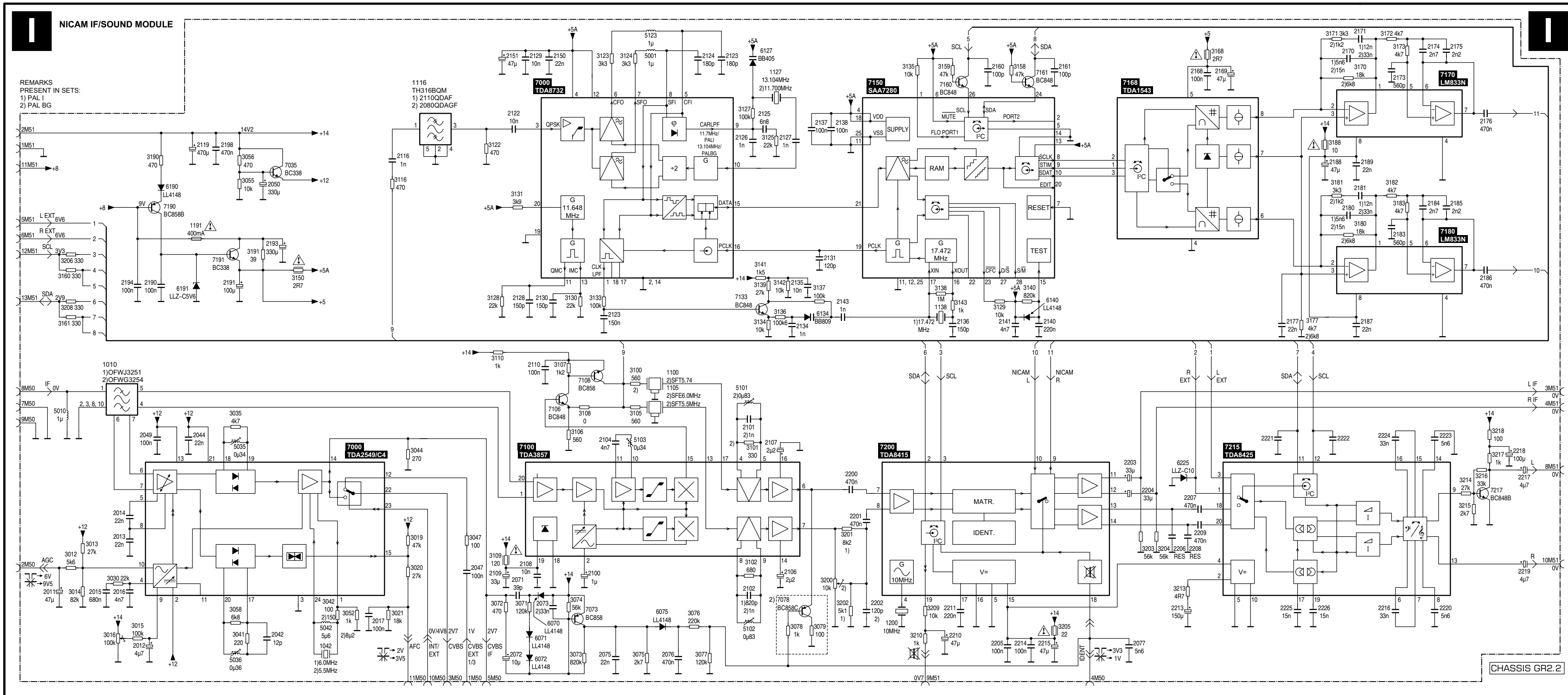
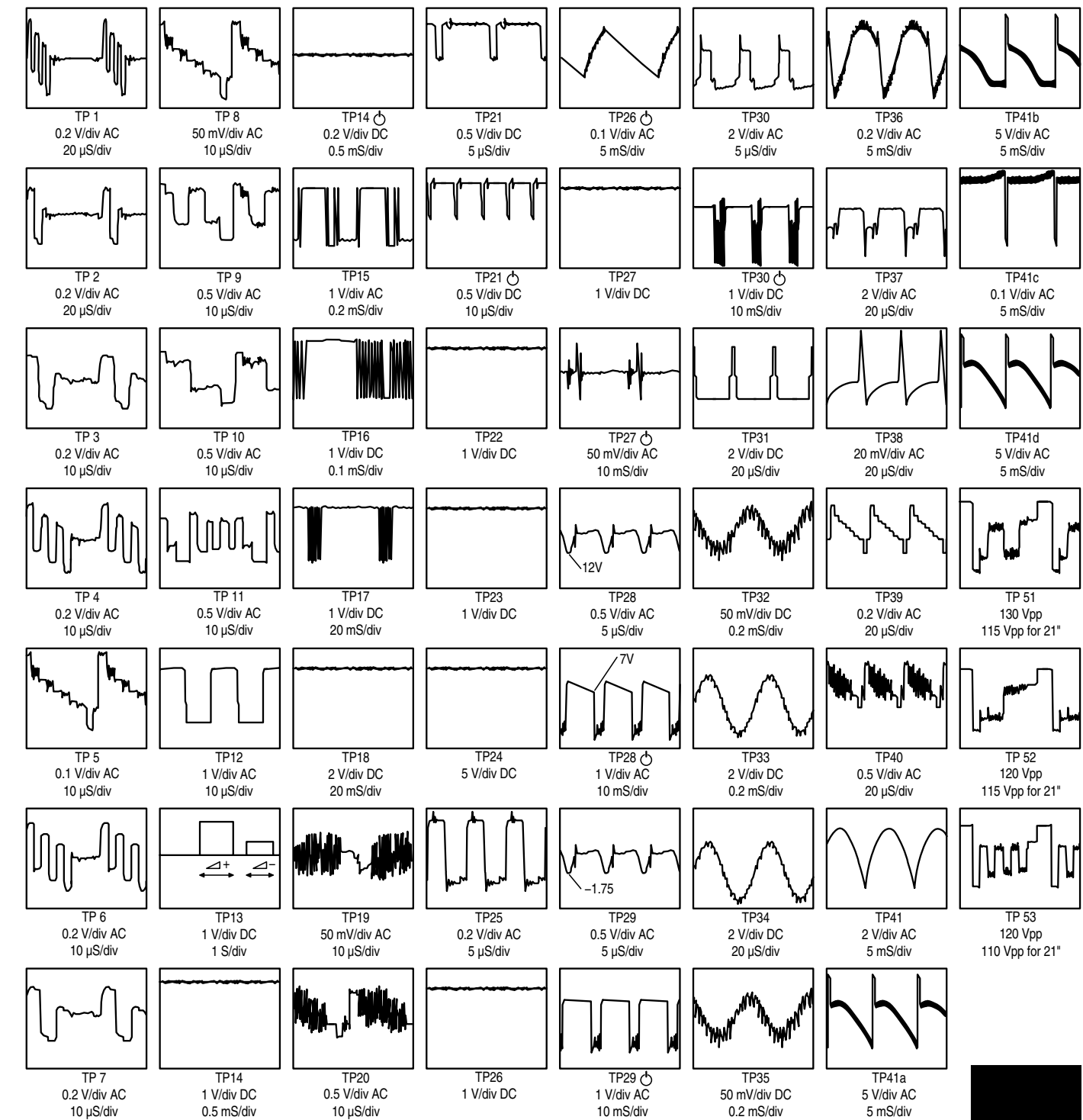
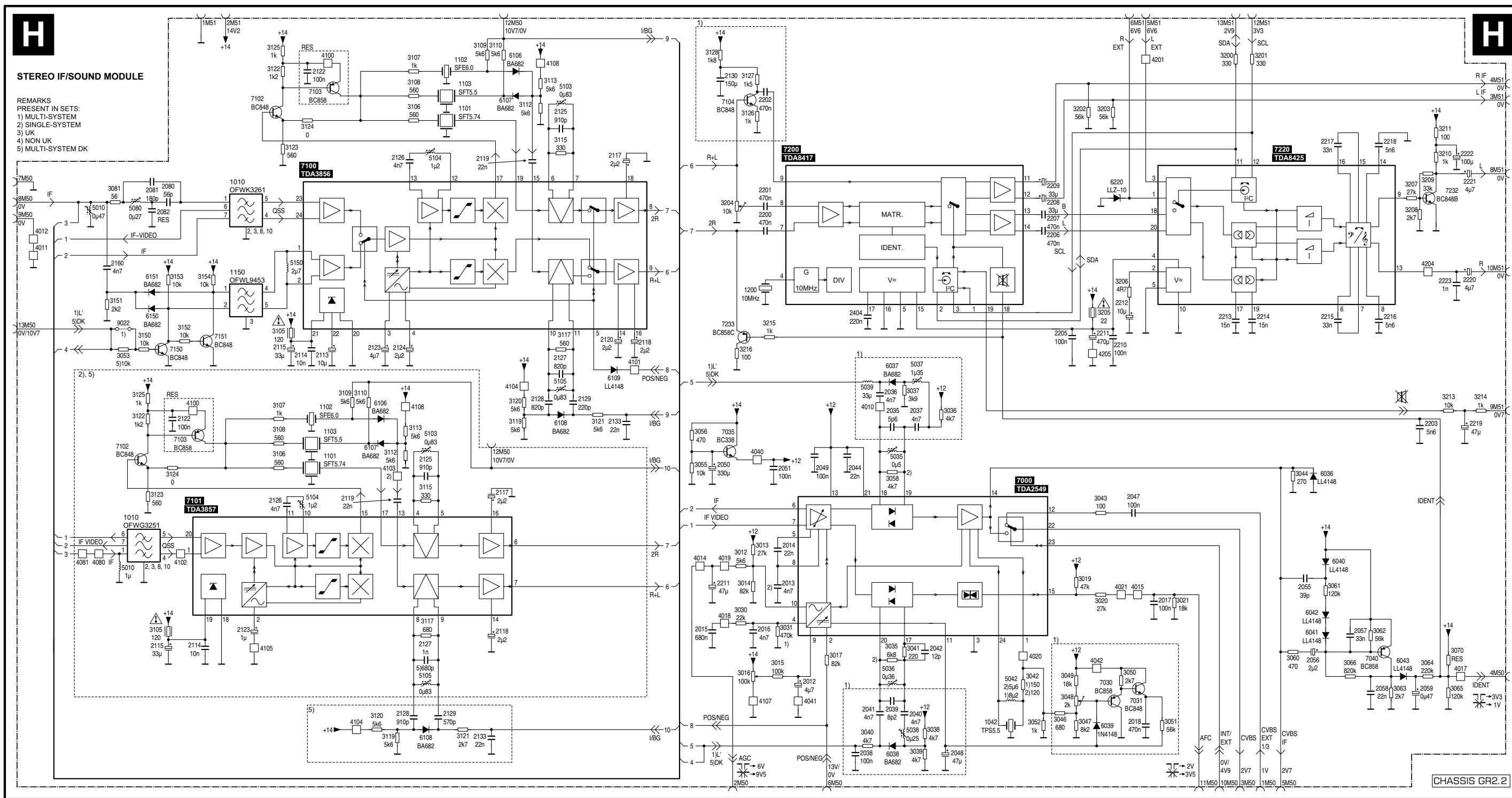


REMARKS
PRESENT IN SETS:
1)MULTI-SYSTEM
2) SINGLE-SYSTEM
3)UK
4) NON UK
5) MULTI-SYSTEM DK



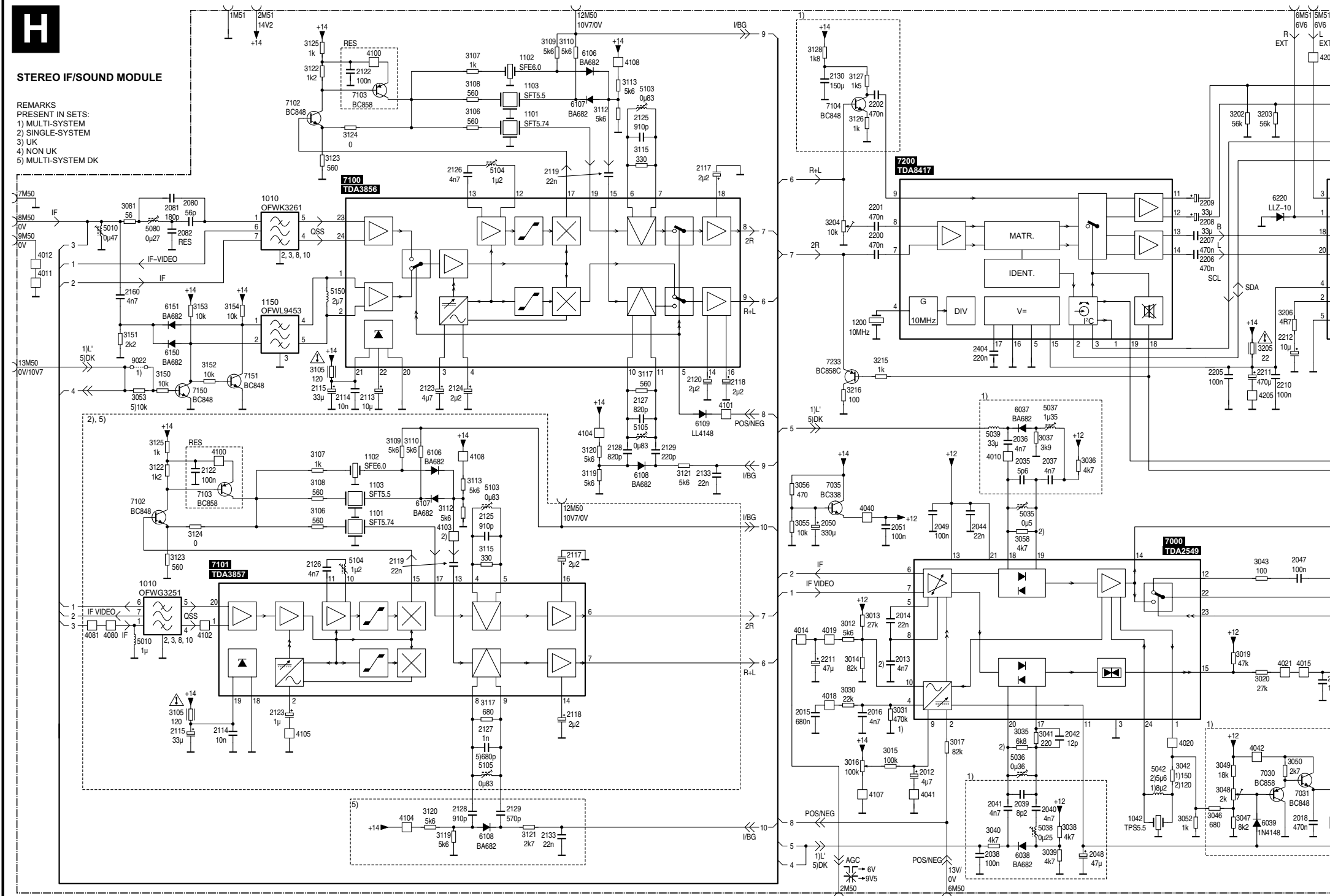
G

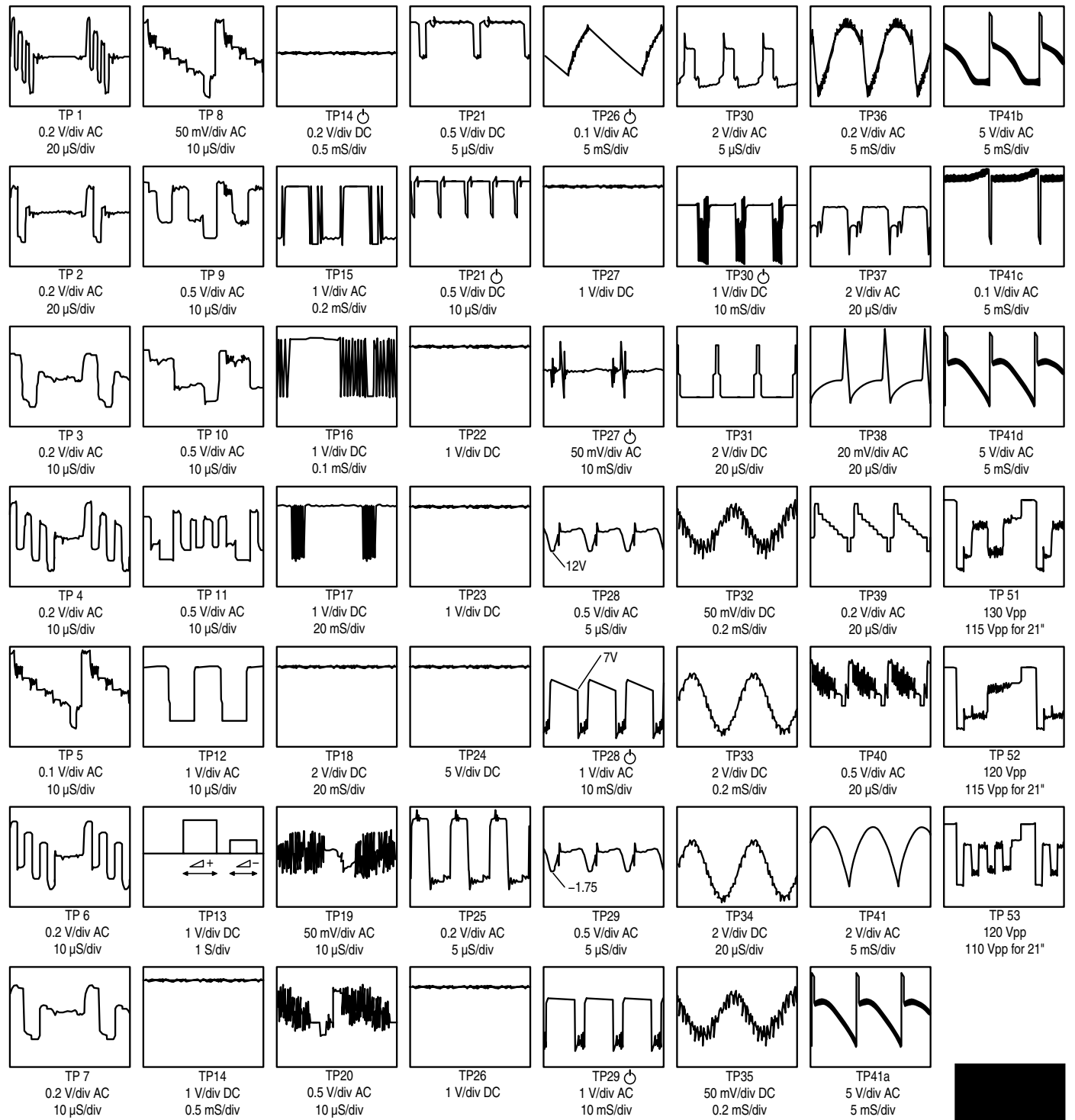
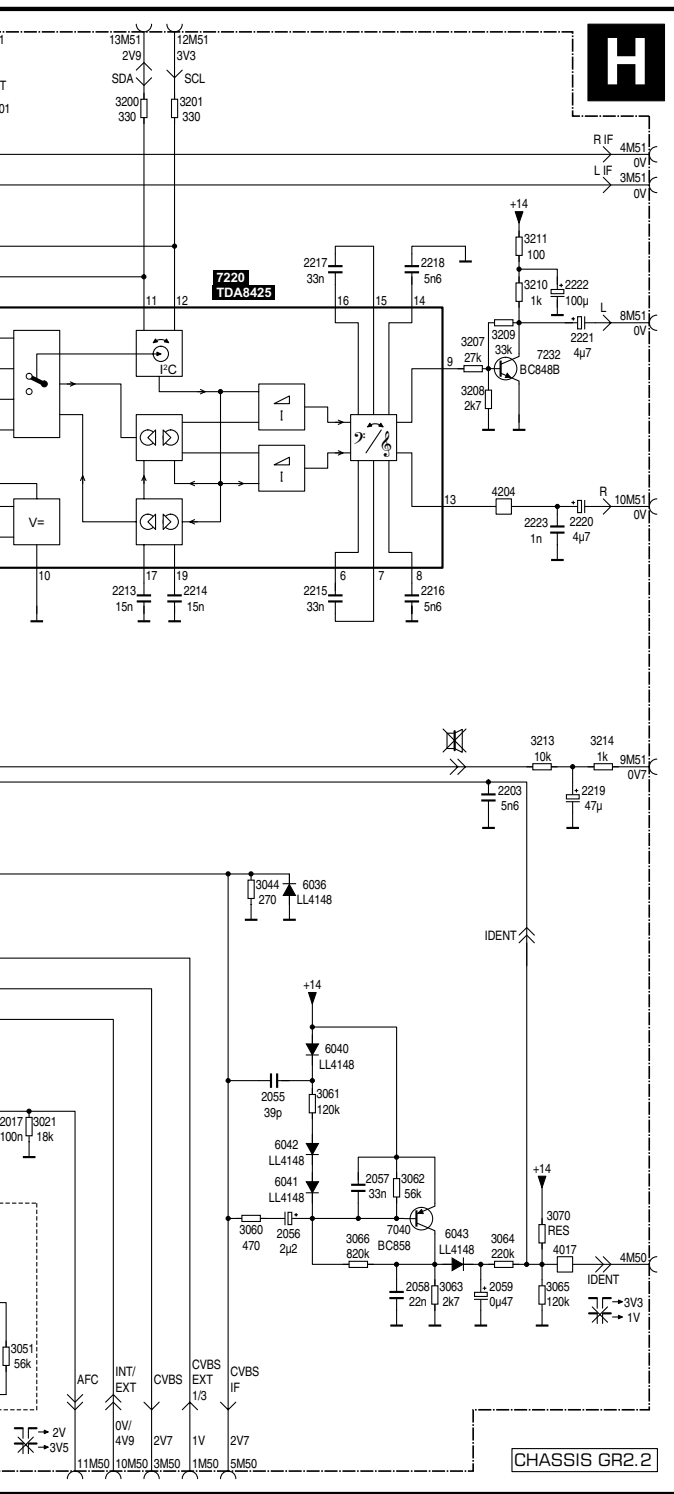






REMARKS
PRESENT IN SETS:
1) MULTI-SYSTEM
2) SINGLE-SYSTEM
3) UK
4) NON UK
5) MULTI-SYSTEM DK

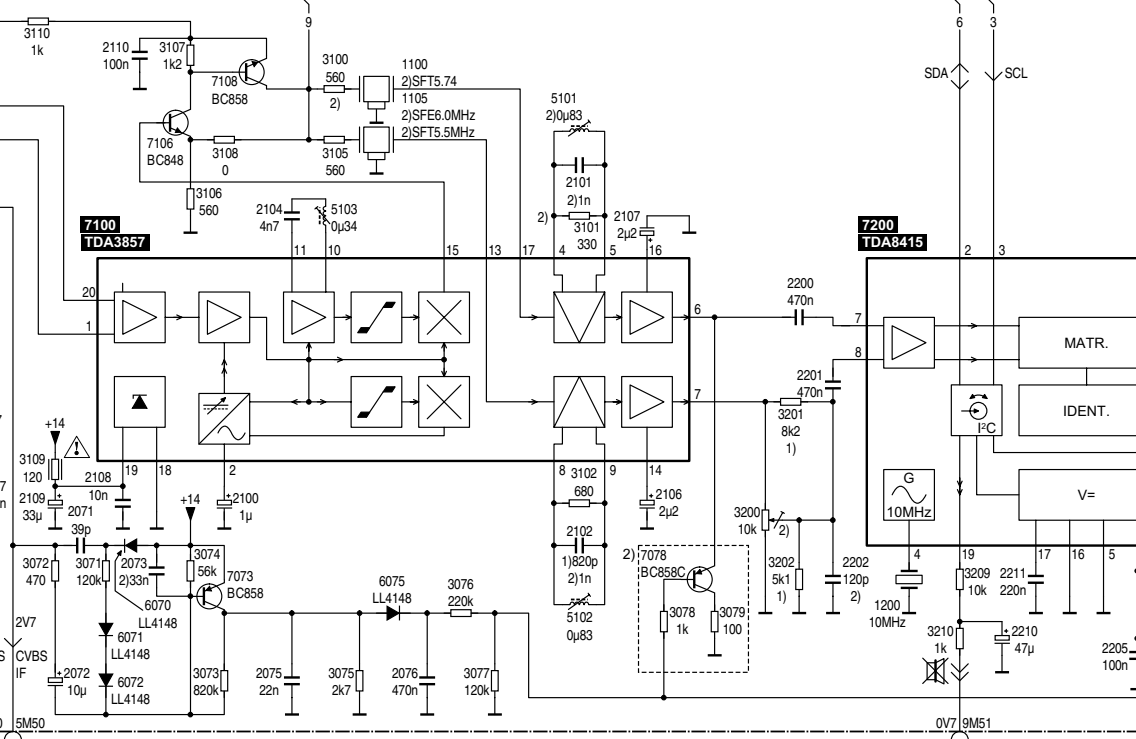
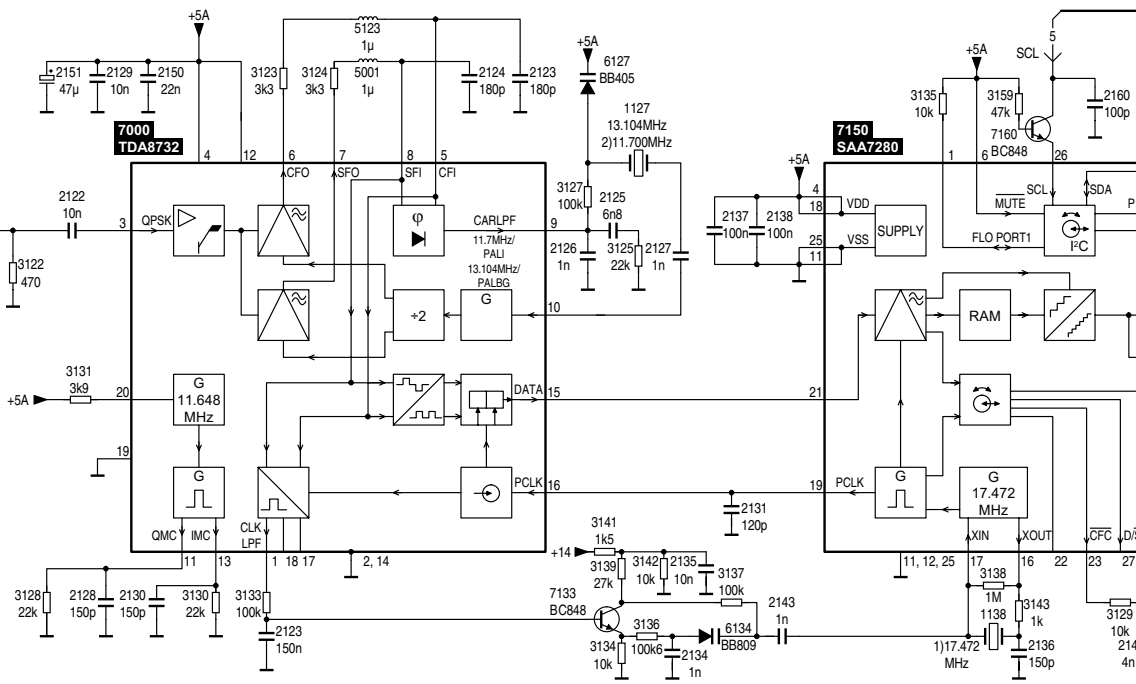
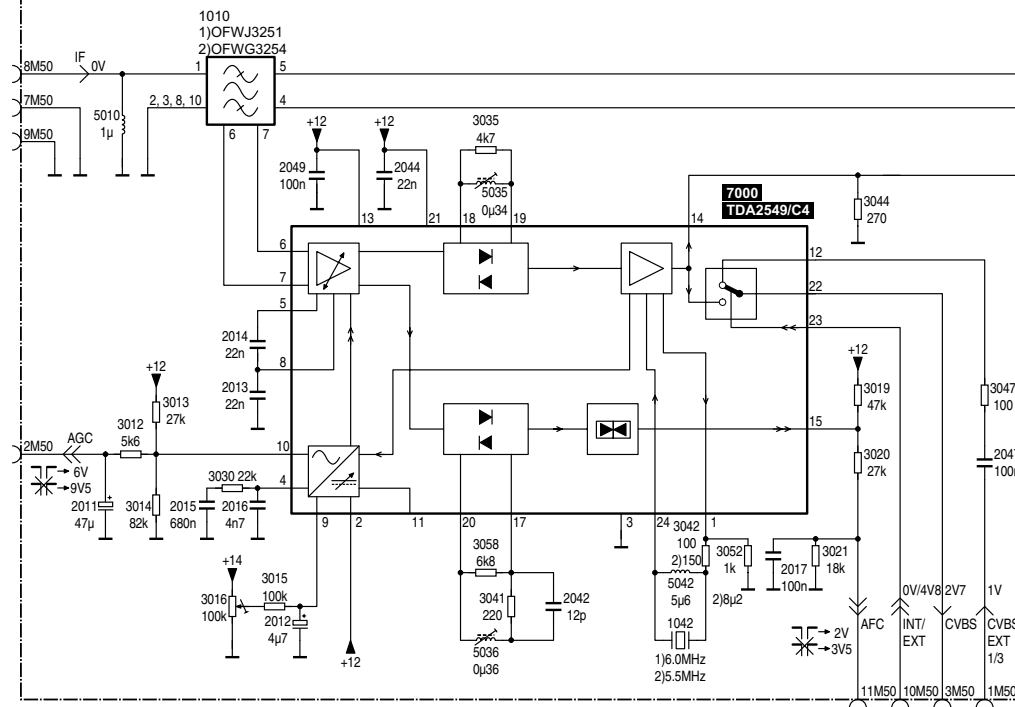
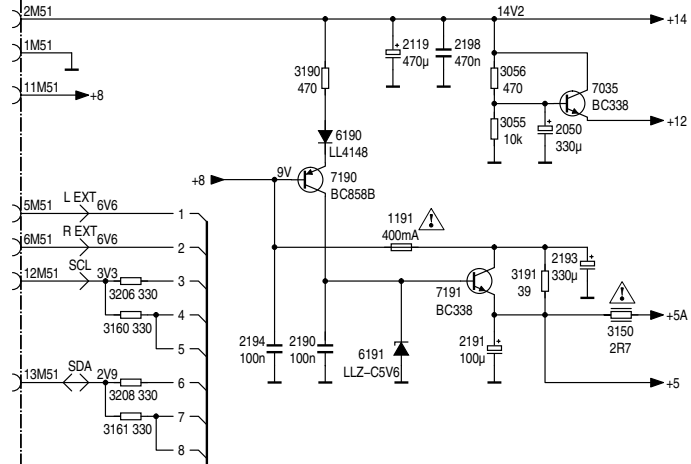


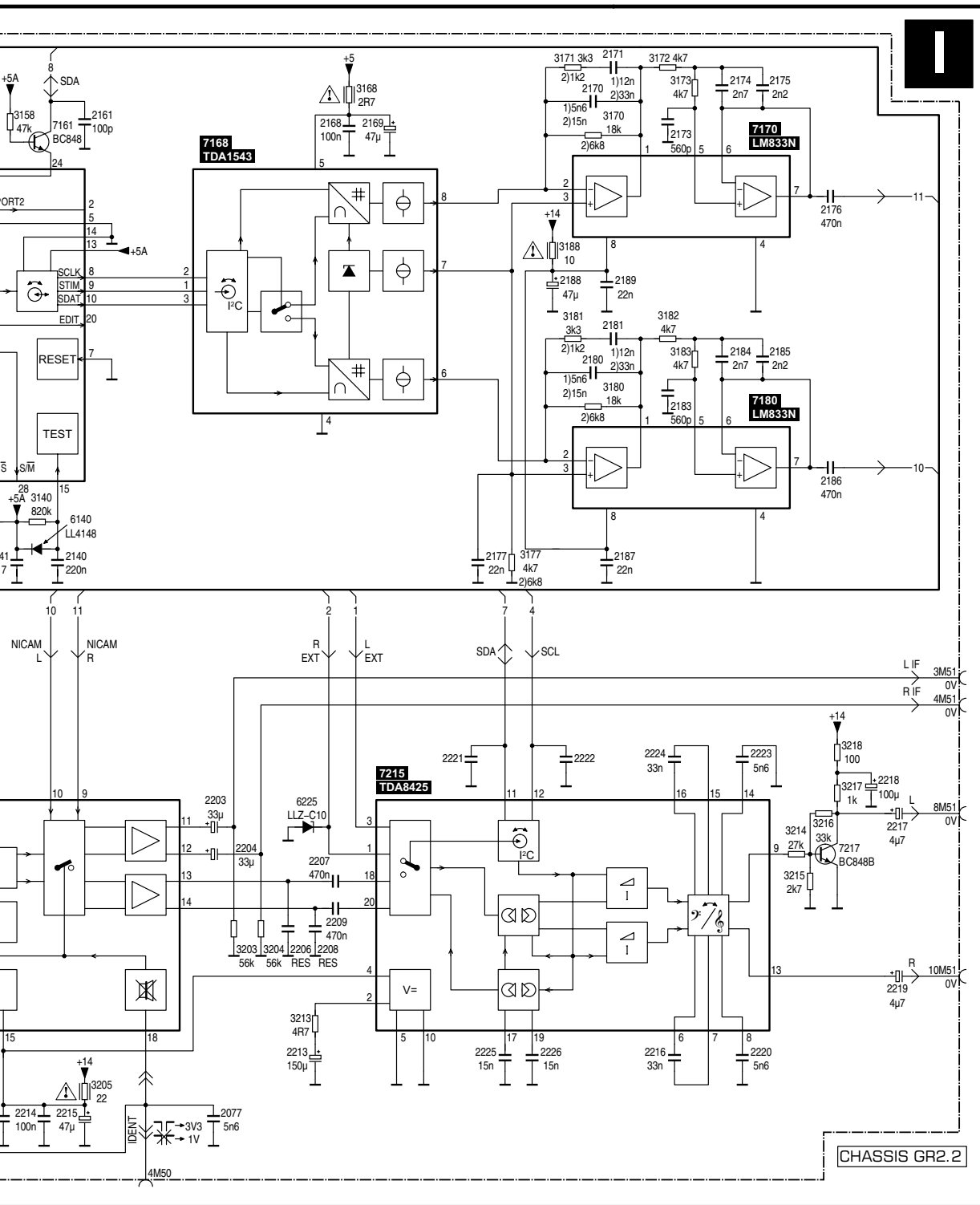




NICAM IF/SOUND MODULE

REMARKS
PRESENT IN SETS:
1) PAL I
2) PAL BG

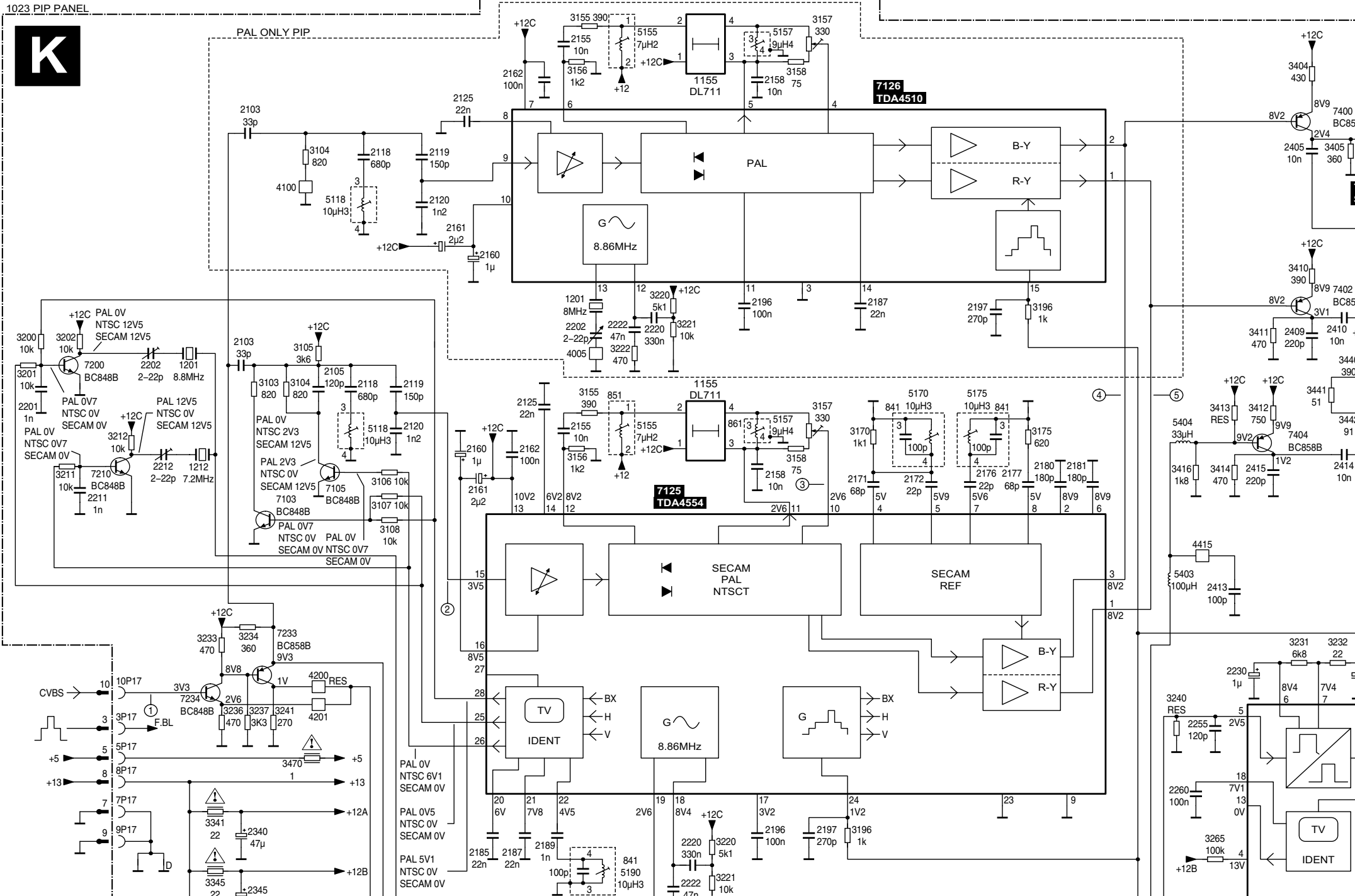


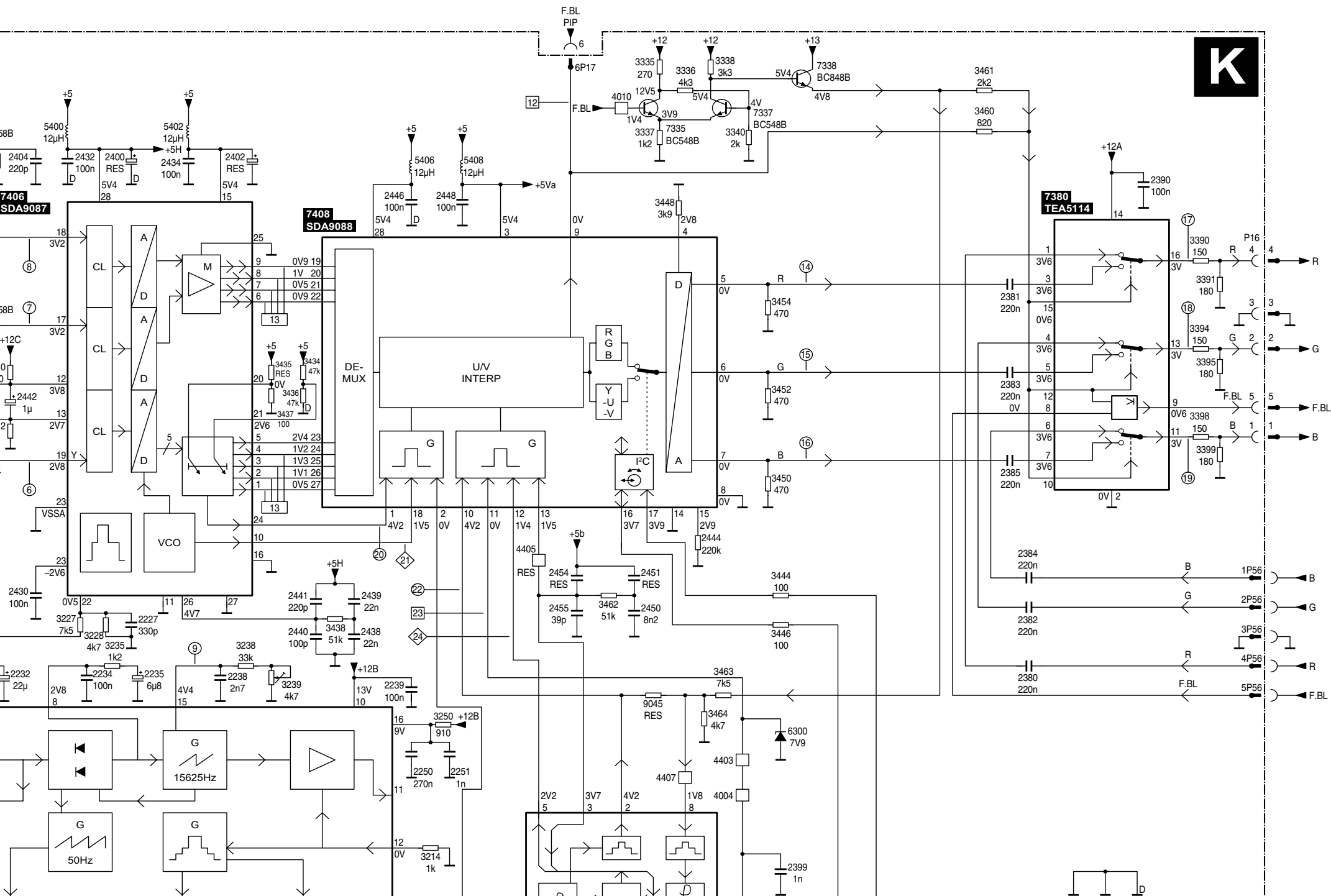


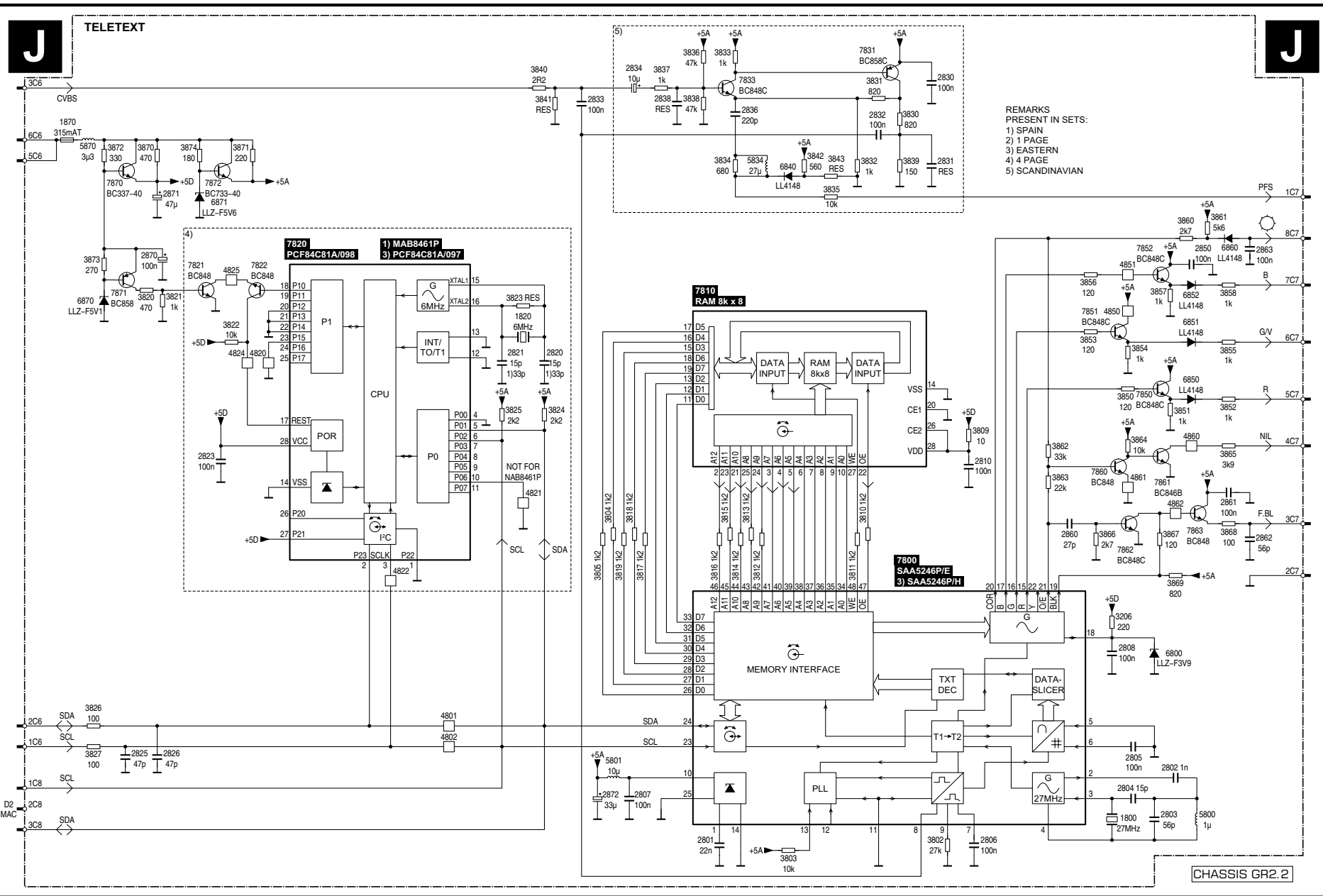
1023 PIP PANEL

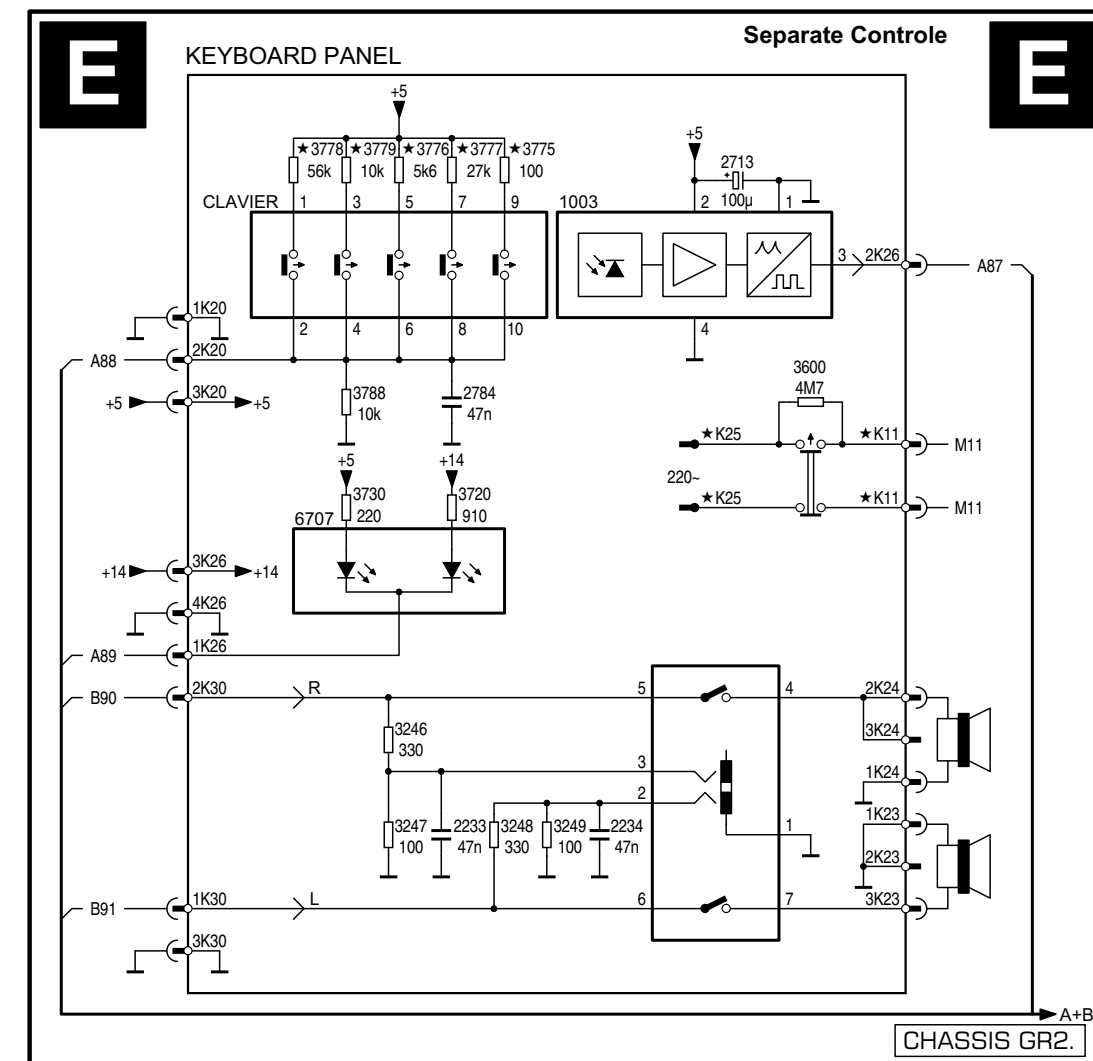
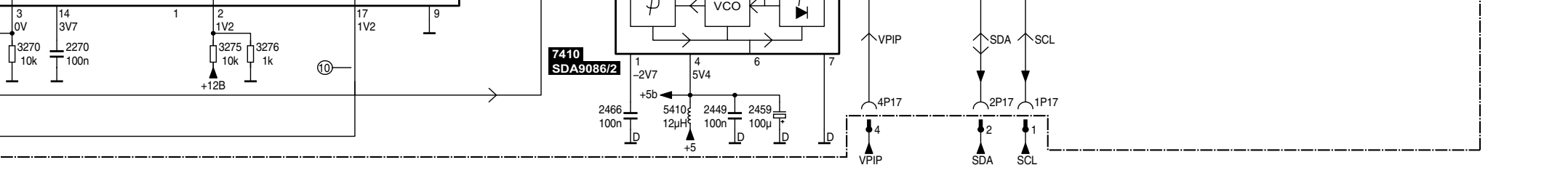


PAL ONLY PIP









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